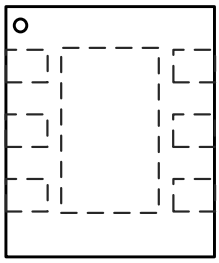
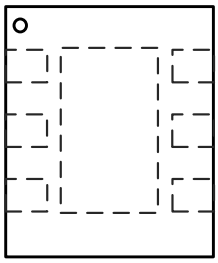
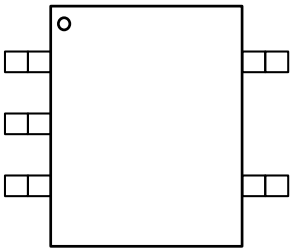
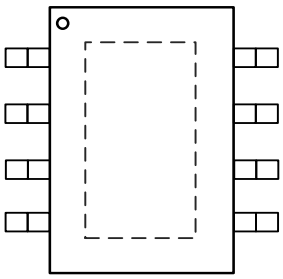
3V-40V Vin, 300mA, 2.4uA I_Q, Low-Dropout Regulator with PG Feature







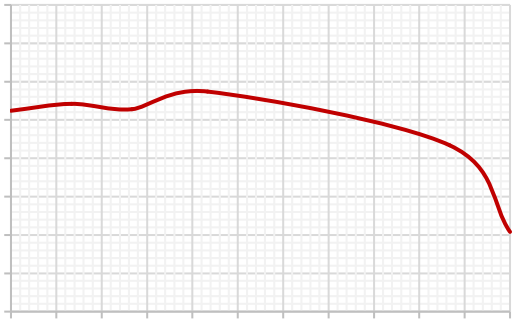


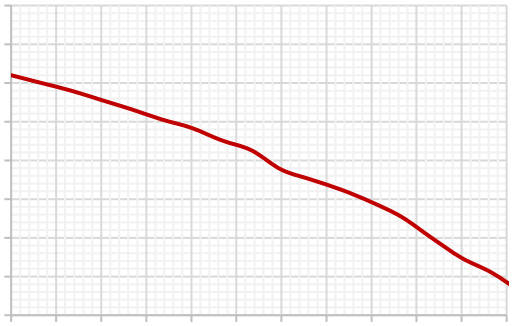
(1)

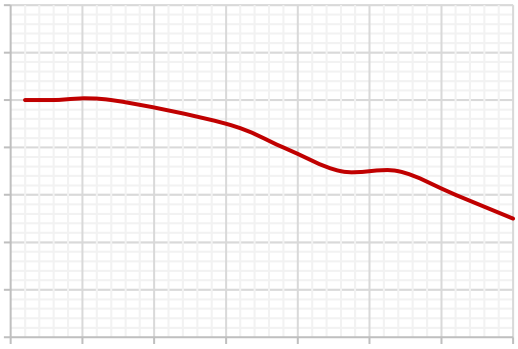
(2)

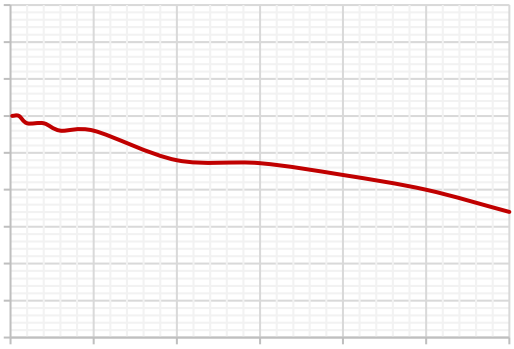
(3)

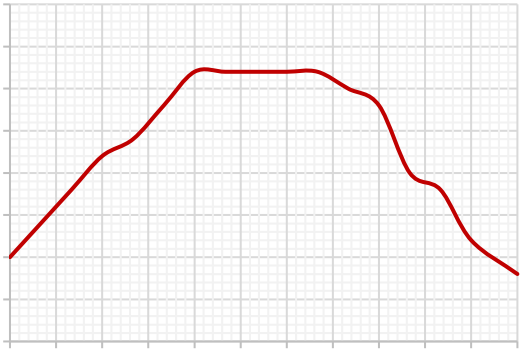
[redacted]				

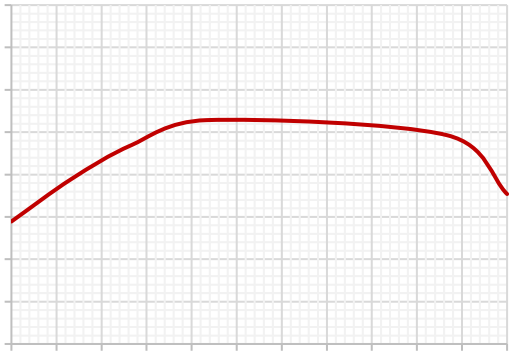


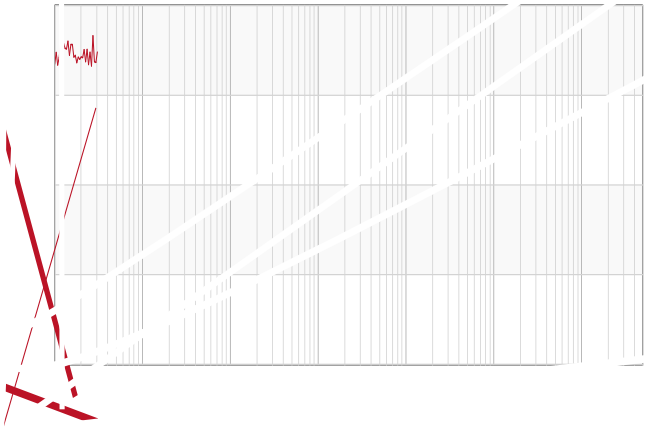


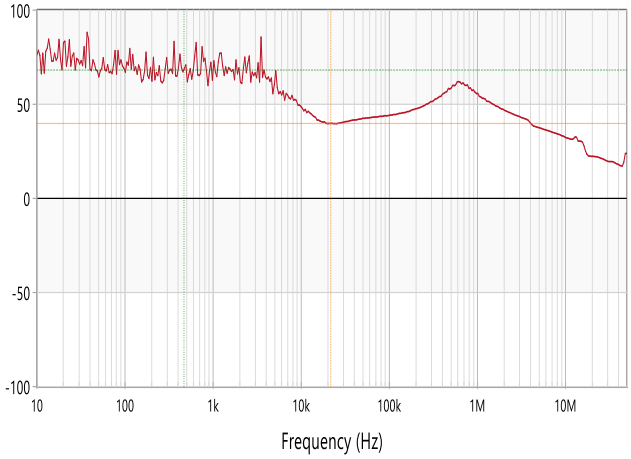


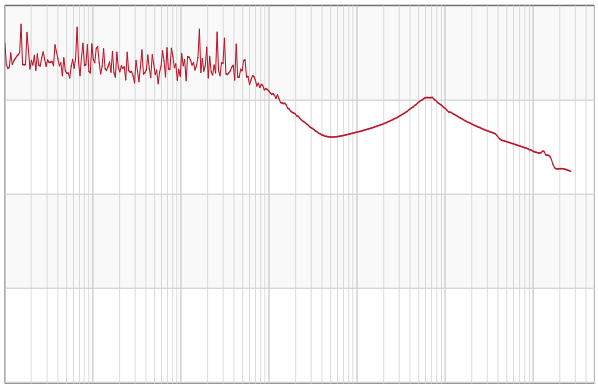


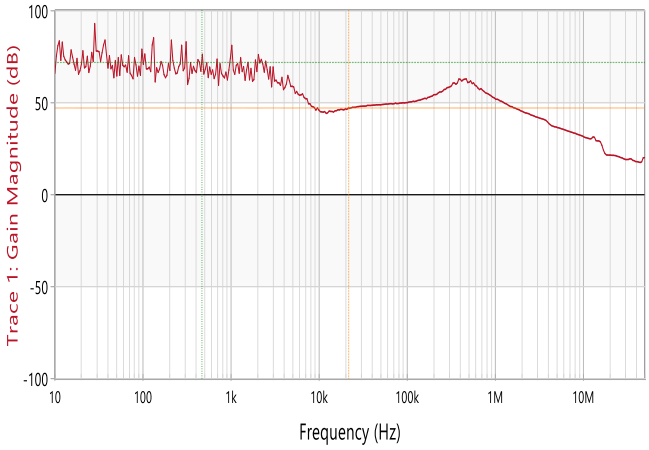


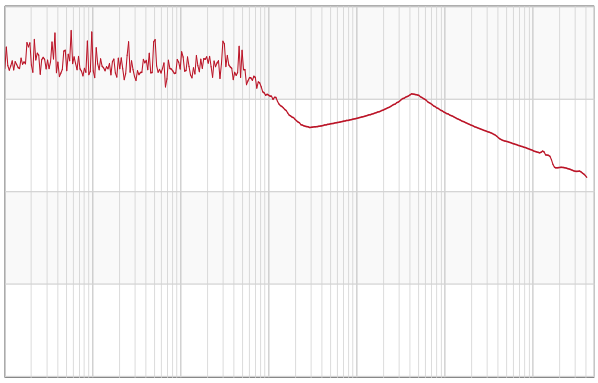


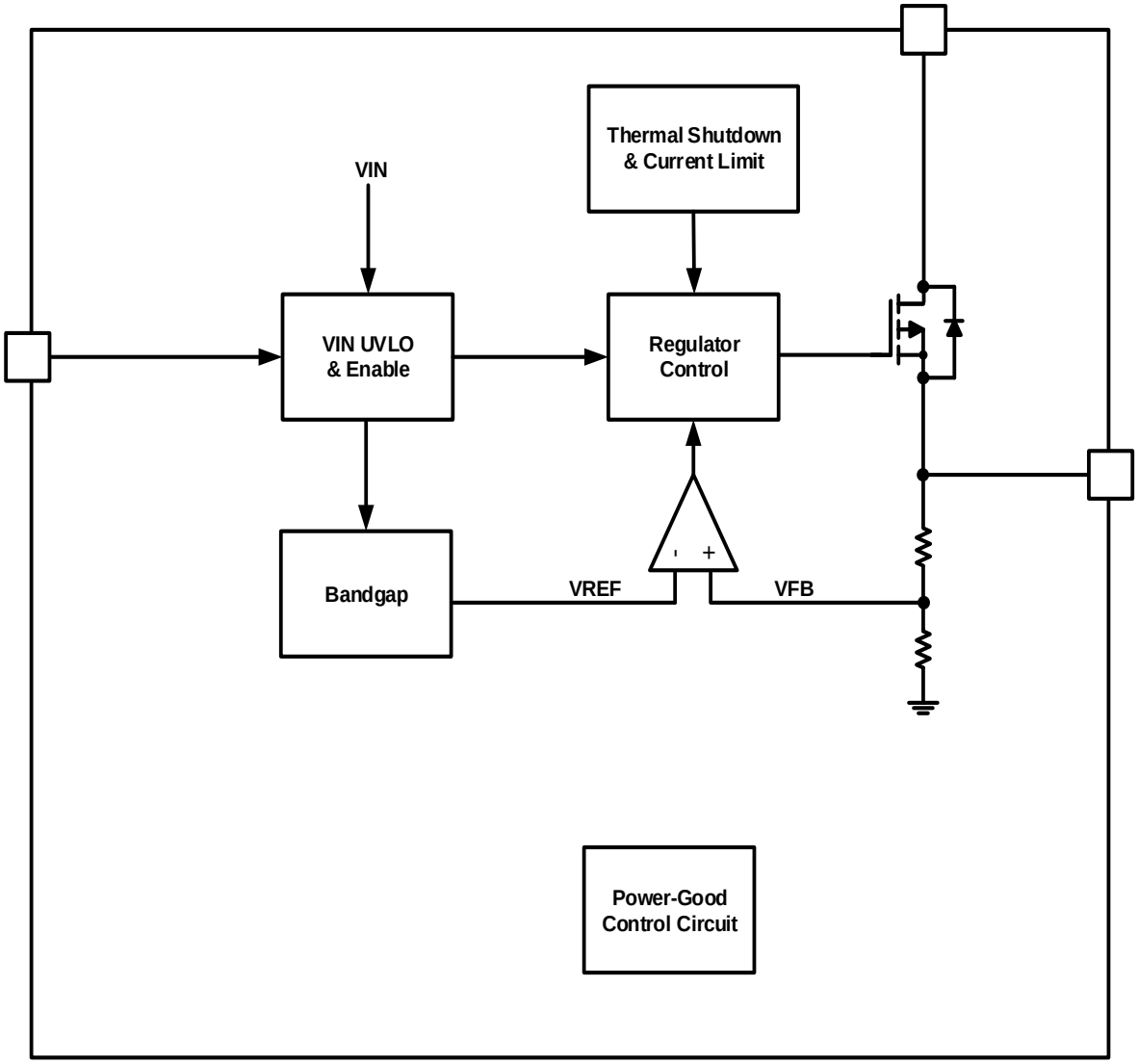


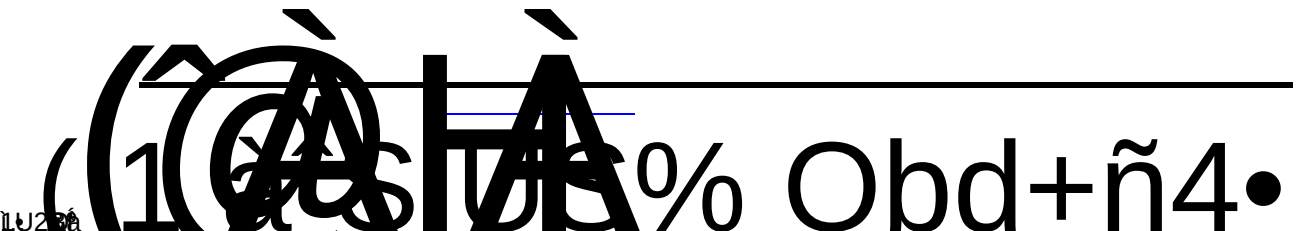

100
50
0
-50
-100
10
100
1k
10k
100k
1M
10M
Frequency (Hz)

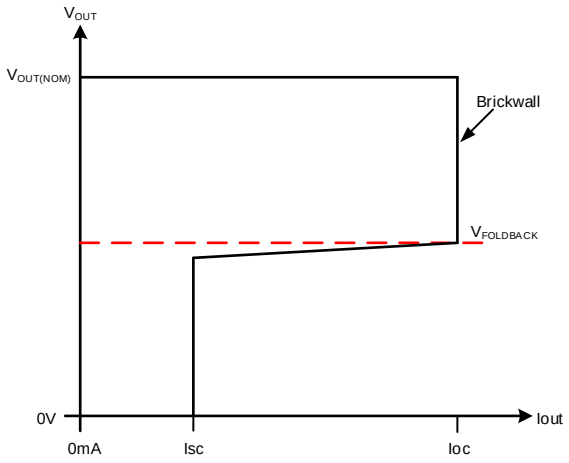


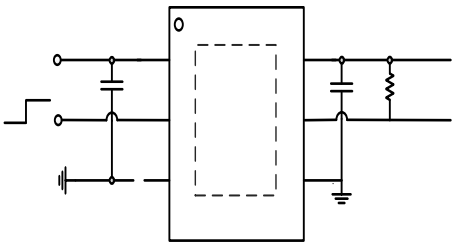

Trace 1: Gain Magnitude (dB)
100
50
0
-50
-100
10
100
1k
10k
100k
1M
10M
Frequency (Hz)

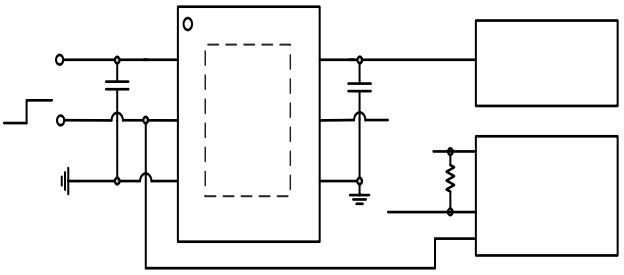


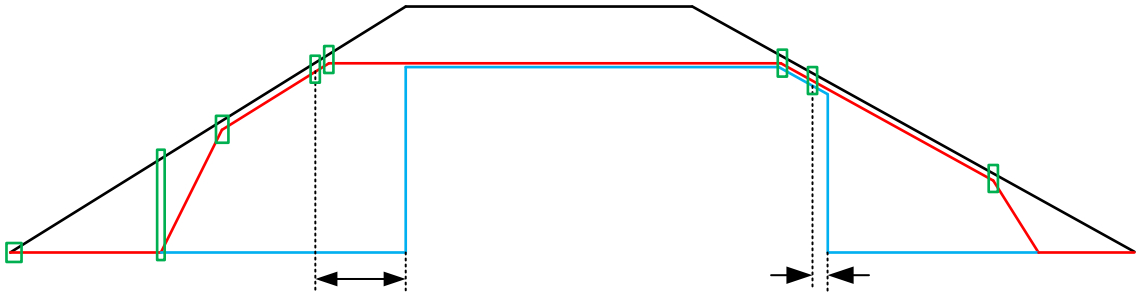

Thermal Shutdown
& Current Limit
VIN
VIN UVLO
& Enable
Regulator
Control
Bandgap
VREF
VFB
Power-Good
Control Circuit

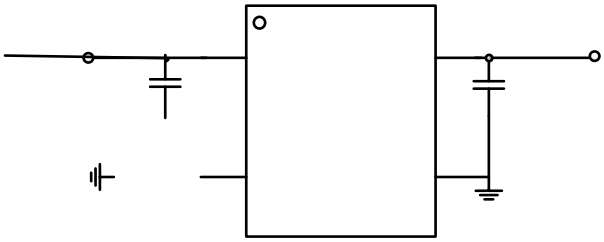


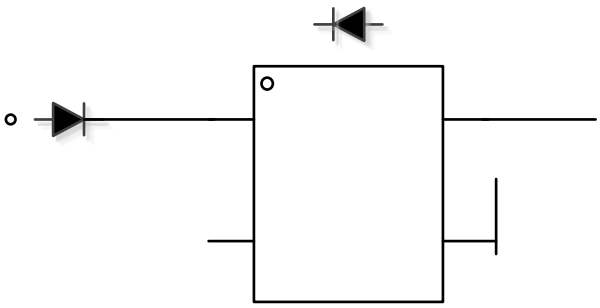

VOUT
VOUT(NOM)
Brickwall
VFOLDBACK
0V
Iout
0mA
Isc
Ioc

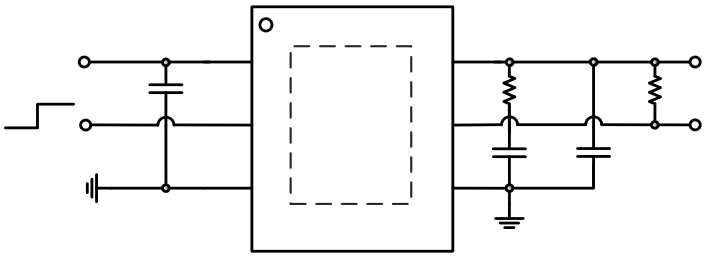




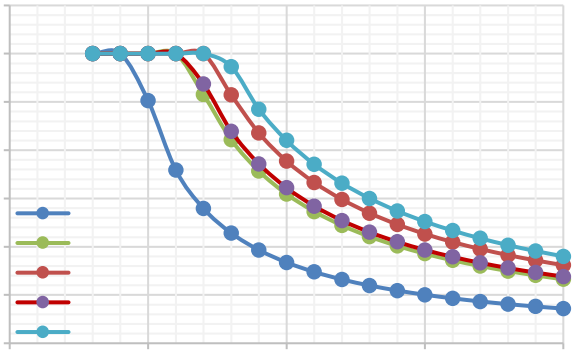


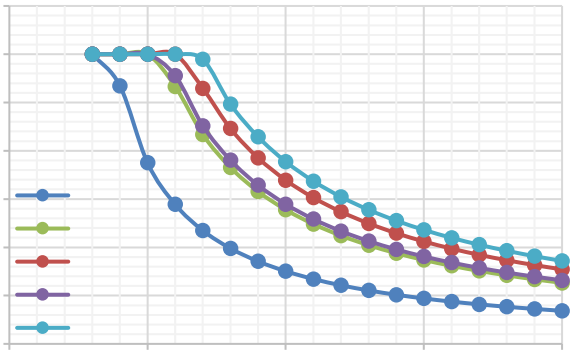


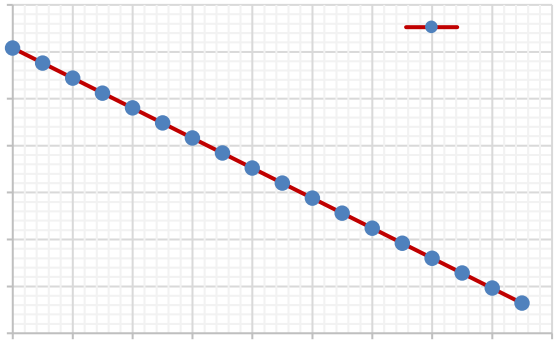


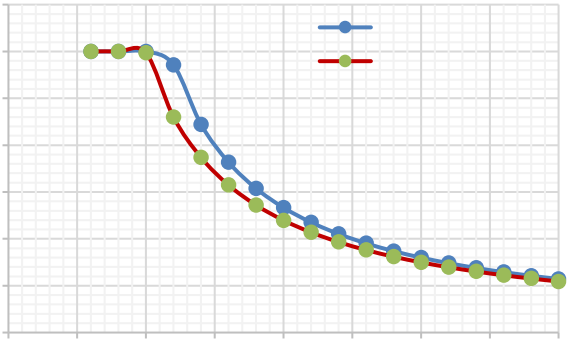


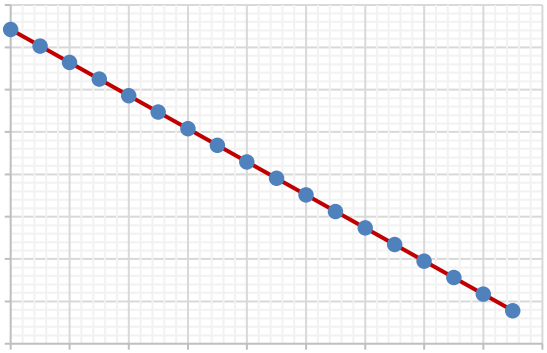
		□



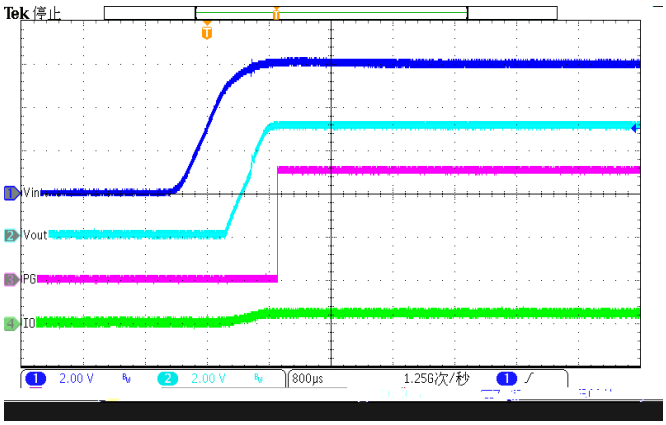


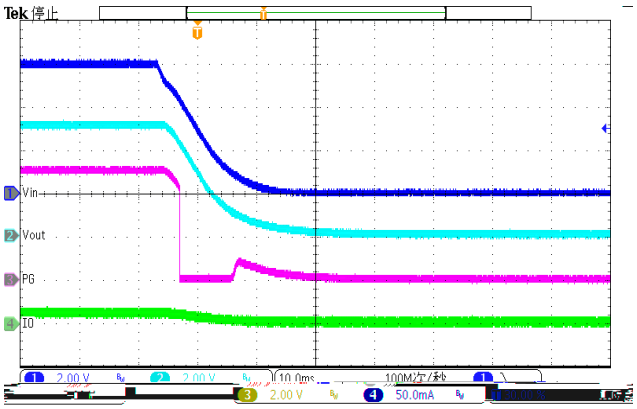


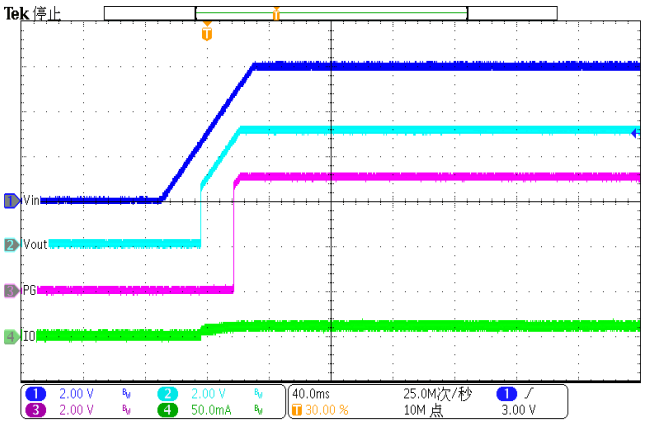


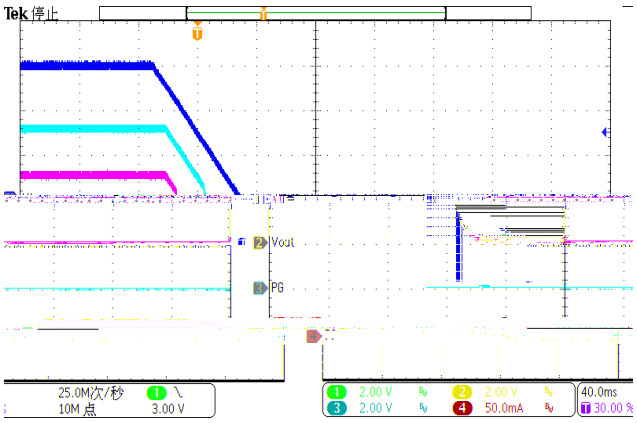


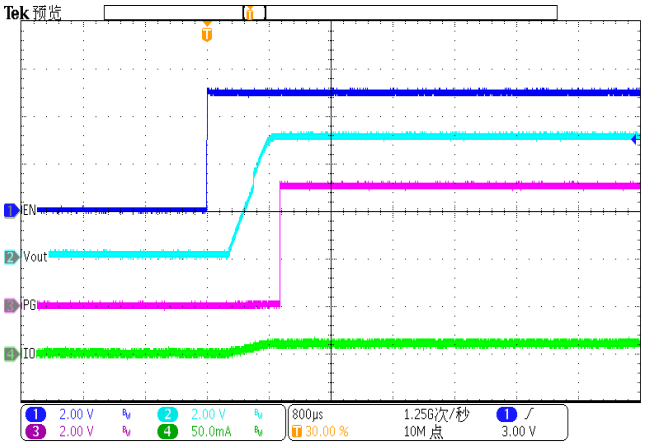
℃

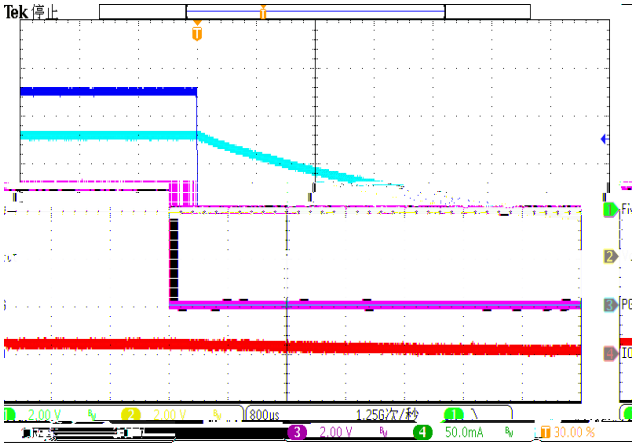

Tek 停止
Vin
Vout
PG
IO
2.00 V
2.00 V
800µs
1.25G次/秒

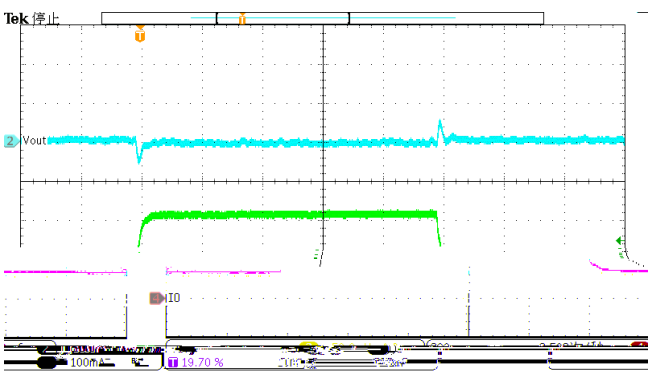

Tek 停止
Vin
Vout
PG
IO
2.00 V
2.00 V
10.0ms
100M次/秒
2.00 V
50.0mA

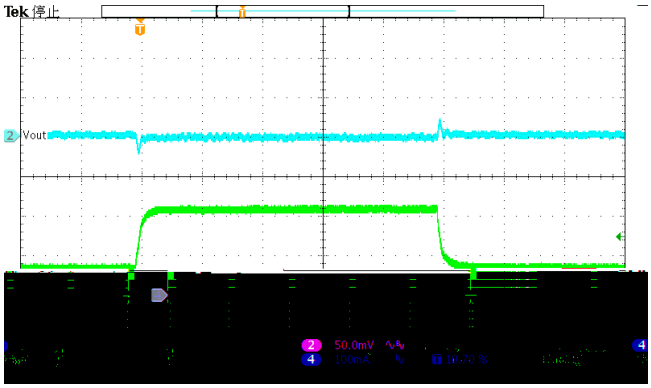

Tek 停止
Vin
Vout
PG
IO
2.00 V
2.00 V
2.00 V
50.0mA
40.0ms
30.00 %
25.0M次/秒
10M 点
3.00 V

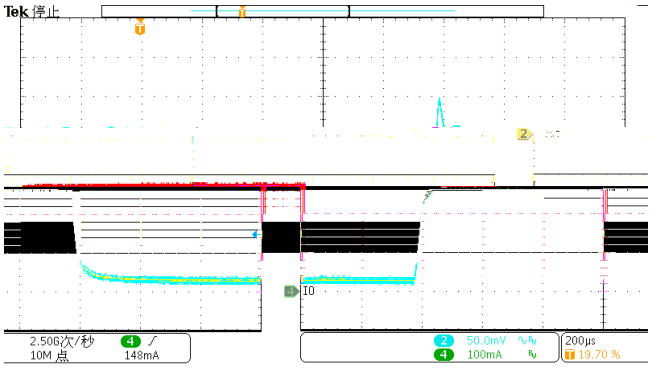

Tek 停止
Vout
PG
25.0M次/秒
10M 点
3.00 V
2.00 V
2.00 V
2.00 V
50.0mA
40.0ms
30.00 %

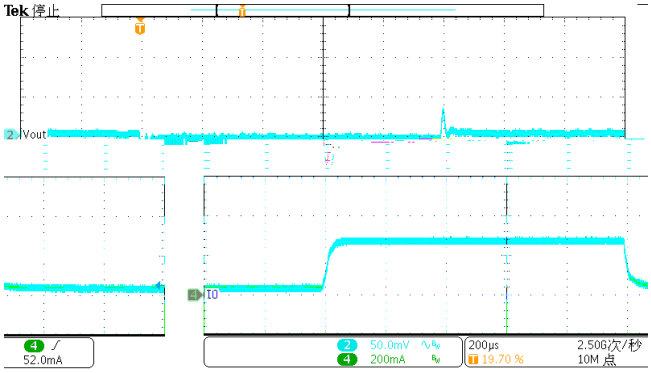

Tek 预览
EN
Vout
PG
IO
2.00 V
2.00 V
2.00 V
50.0mA
800µs
30.00 %
1.25G次/秒
10M 点
3.00 V

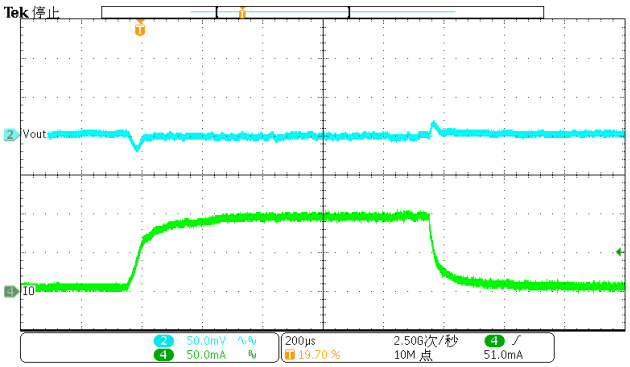

Tek 停止
2.00 V
2.00 V
800µs
1.25G次/秒
2.00 V
50.0mA
30.00 %

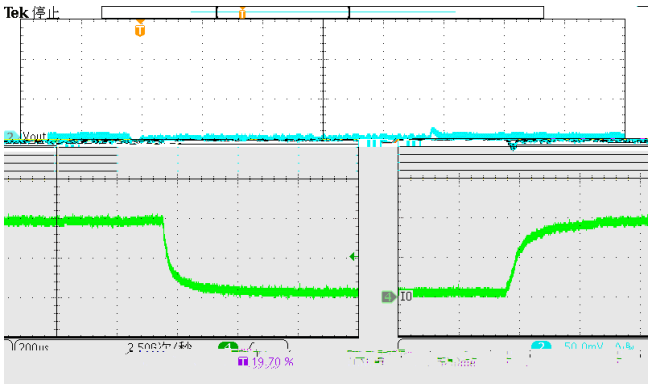

Tek 停止
Vout
IO
100mA
19.70 %

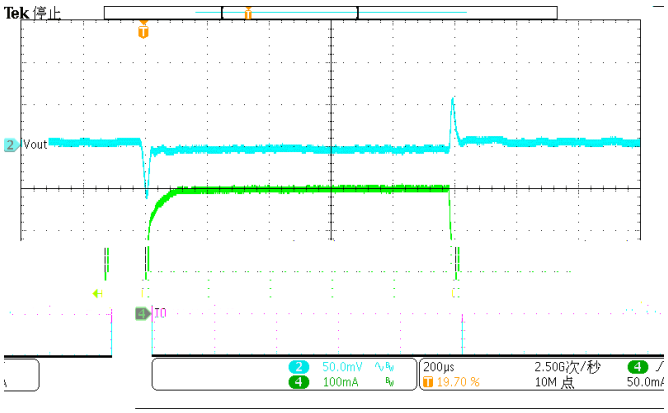

Tek 停止
Vout
50.0mV
100mA

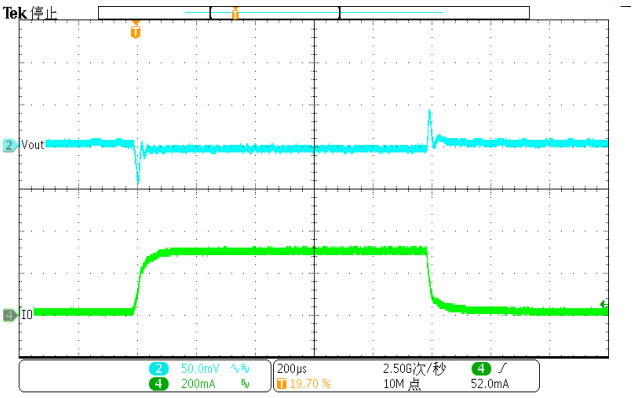

Tek 停止
IO
2.50G次/秒
10M 点
148mA
50.0mV
100mA
200µs
19.70 %

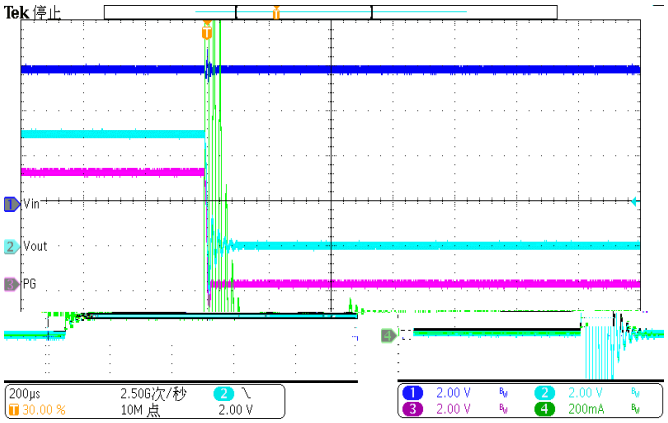

Tek 停止
Vout
IO
52.0mA
50.0mV
200mA
200µs
19.70 %
2.50G次/秒
10M 点

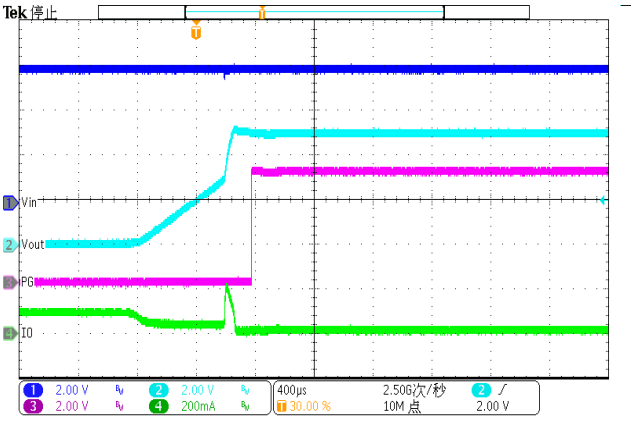

Tek 停止
Vout
IO
50.0mV
50.0mA
200µs
19.70 %
2.50G次/秒
10M 点
51.0mA

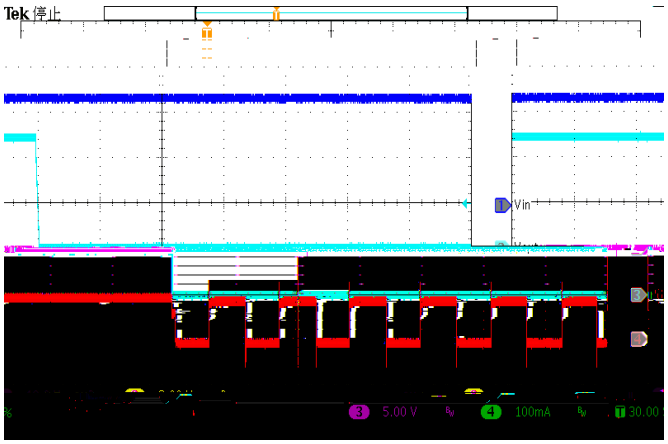

Tek 停止
Vout
IO
19.70 %

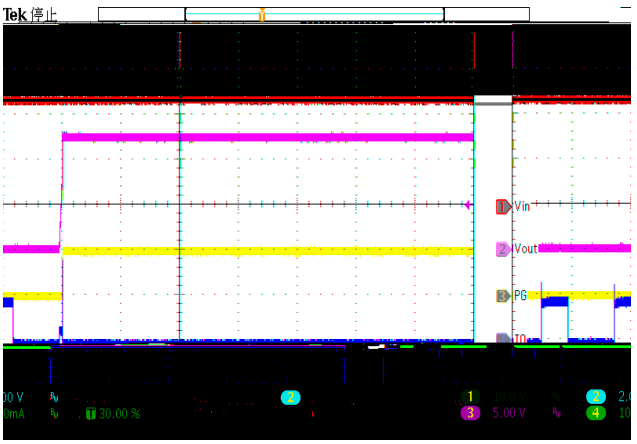

Tek 停止
Vout
IO
50.0mV
100mA
200µs
19.70 %
2.50G次/秒
10M 点
50.0mA


Tek 停止
Vout
IO
50.0mV
200mA
200µs
19.70 %
2.50G次/秒
10M 点
52.0mA


Tek 停止
Vin
Vout
PG
200µs
30.00 %
2.50G次/秒
10M 点
2.00 V
2.00 V
2.00 V
2.00 V
200mA


Tek 停止
Vin
Vout
PG
IO
2.00 V
2.00 V
2.00 V
2.00 V
200mA
400µs
30.00 %
2.50G次/秒
10M 点
2.00 V

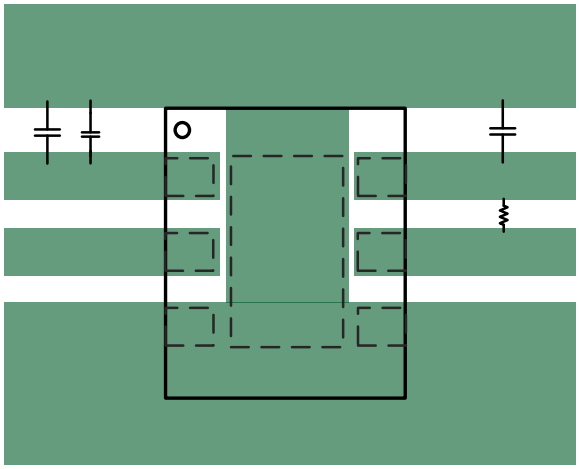

Tek 停止
Vin
5.00 V
100mA

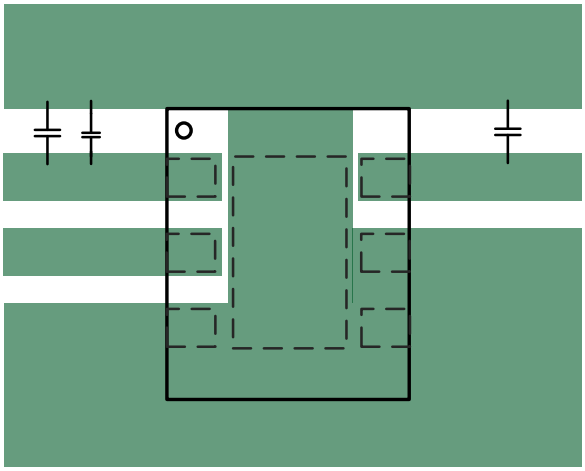

Tek 停止
Vin
Vout
PG
IO
30.00 %
5.00 V

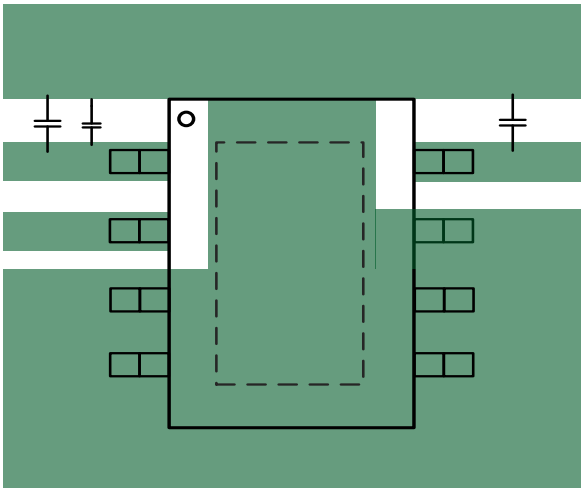
1.
2.

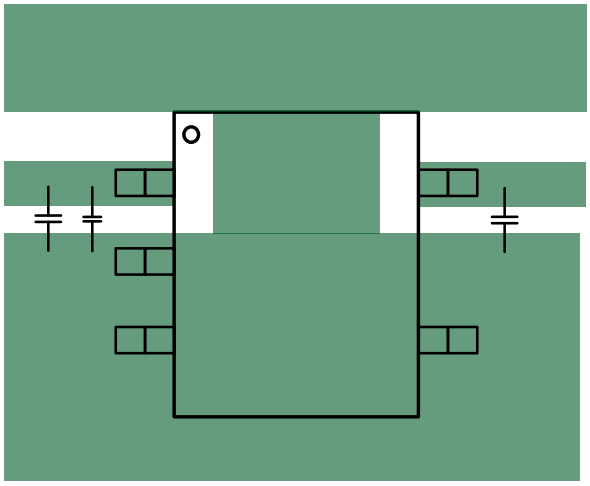
3.
4.

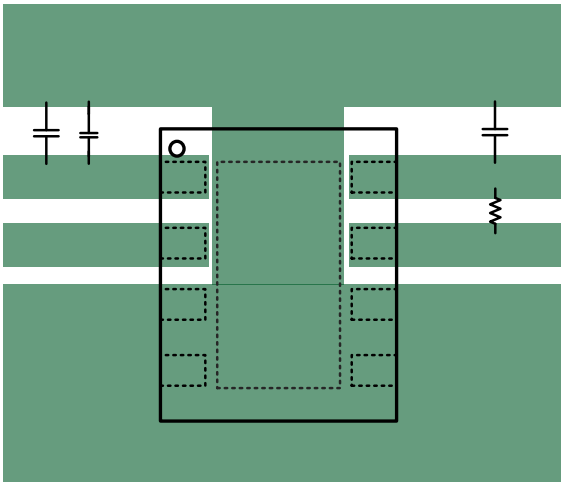
5.

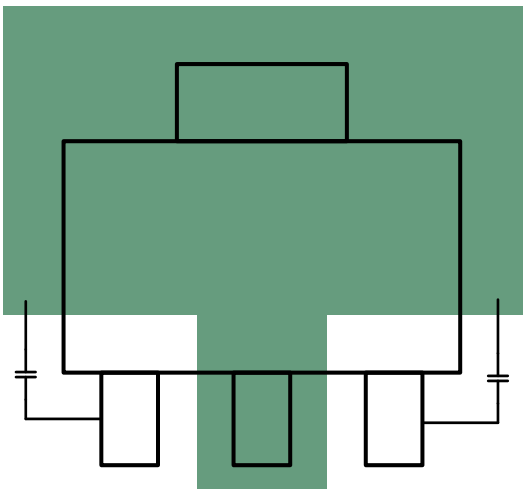


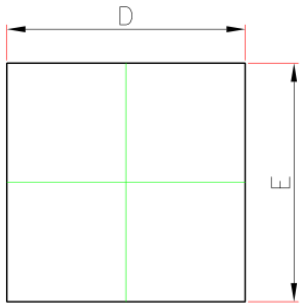




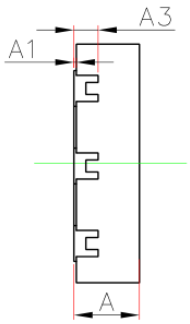




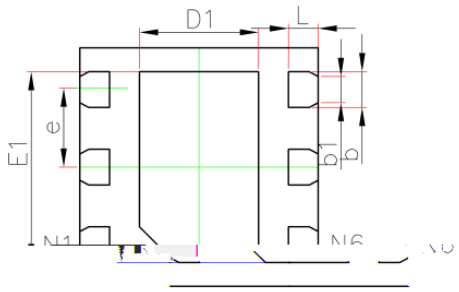




TOP VIEW

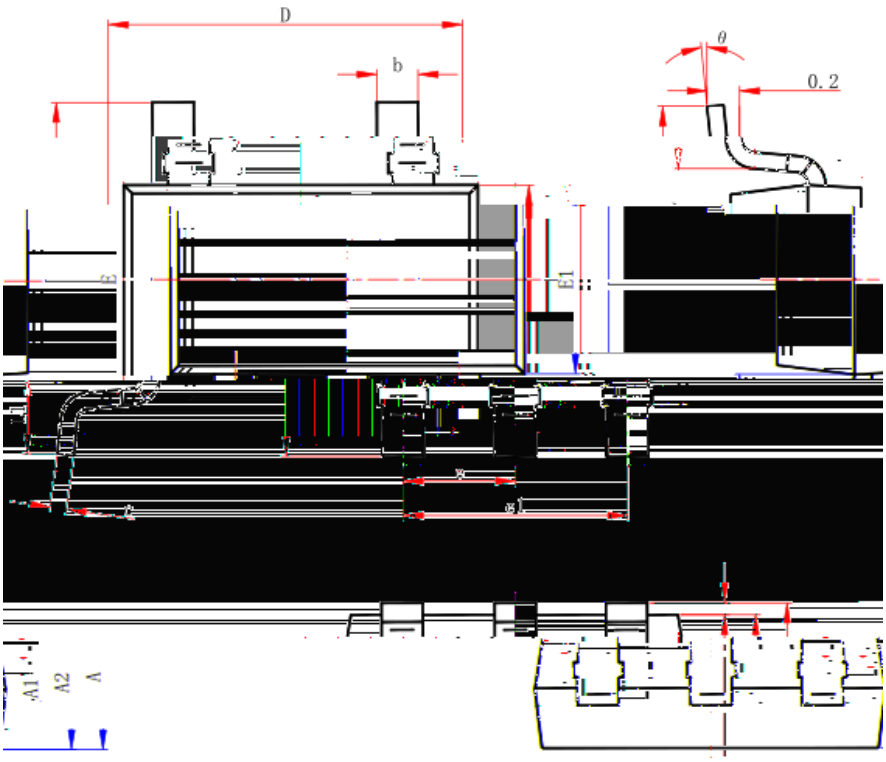


SIDE VIEW

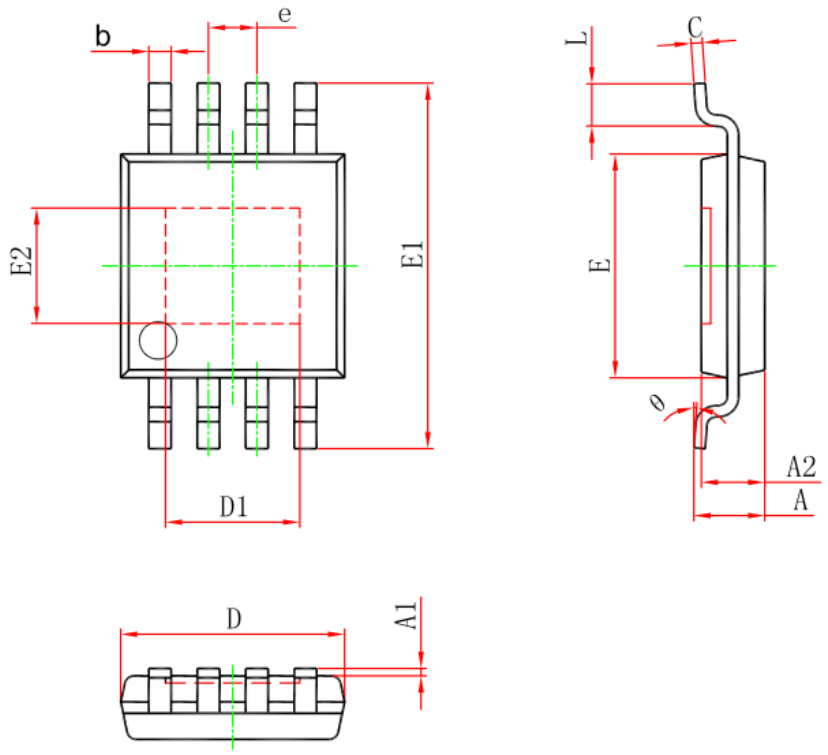


BOTTOM VIEW

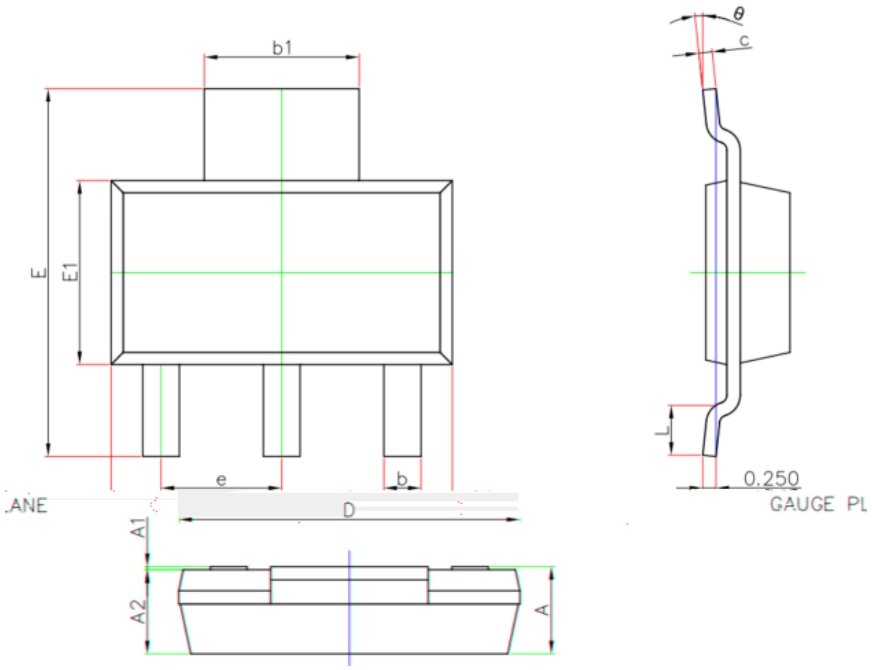
1.
2.
3.
4.
5.
6.



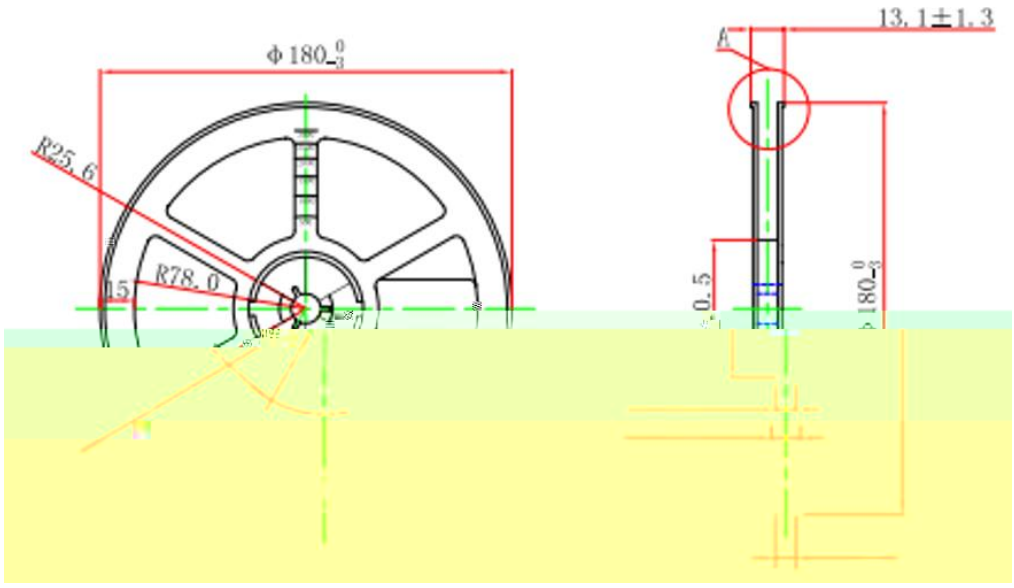
1.
2.
3.
4.
5.
6.

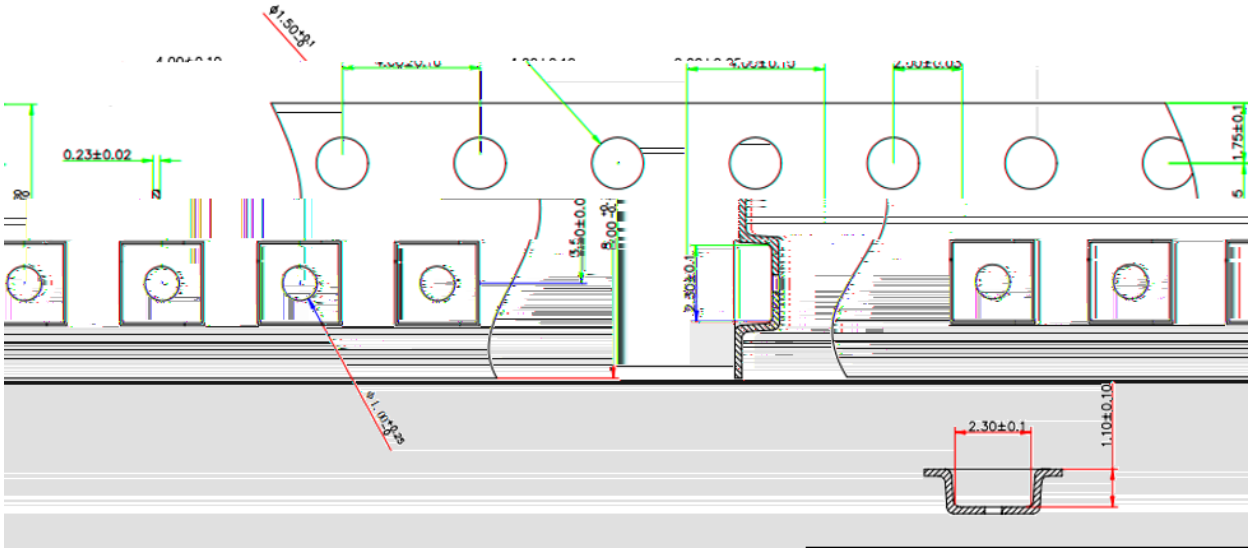


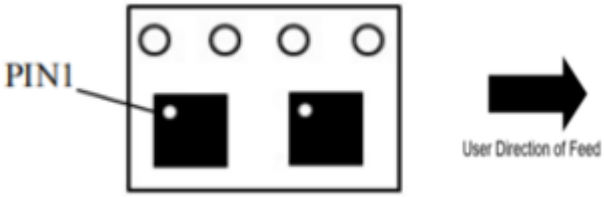
1.
2.
3.
4.
5.
6.

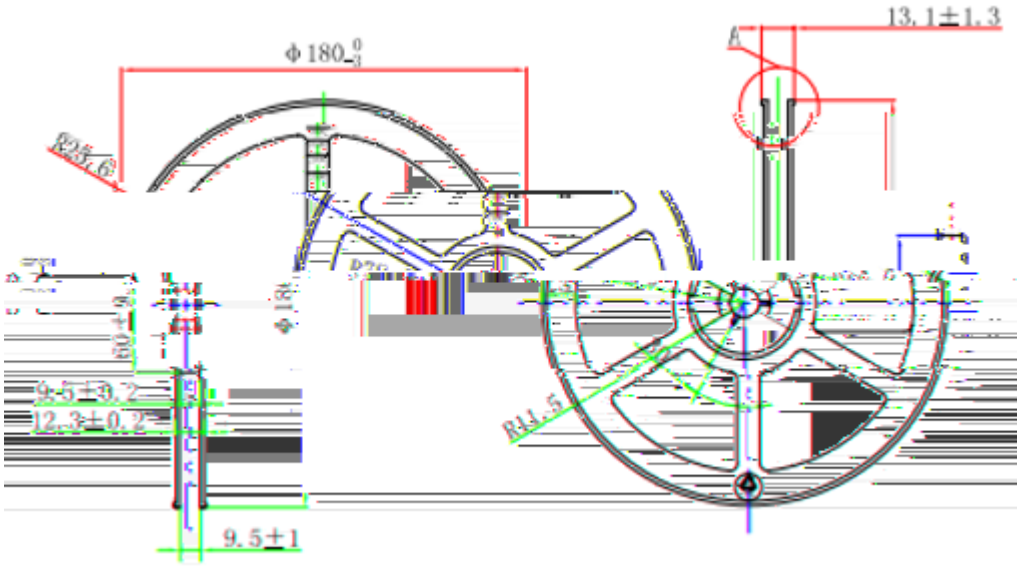


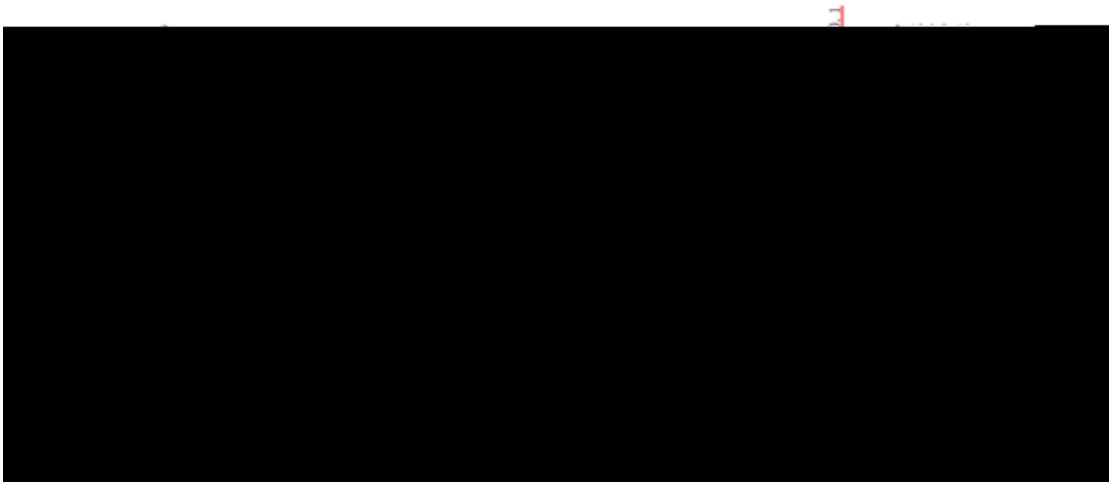
7.
8.
9.
10.
11.
12.

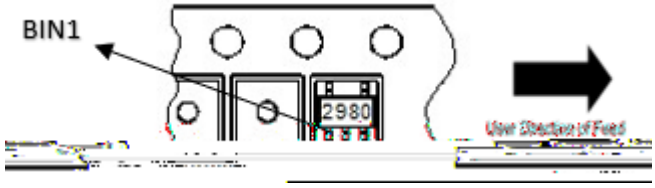

13.1±1.3
A
Φ180
R25.6
R78.0
15
0.5
Φ180

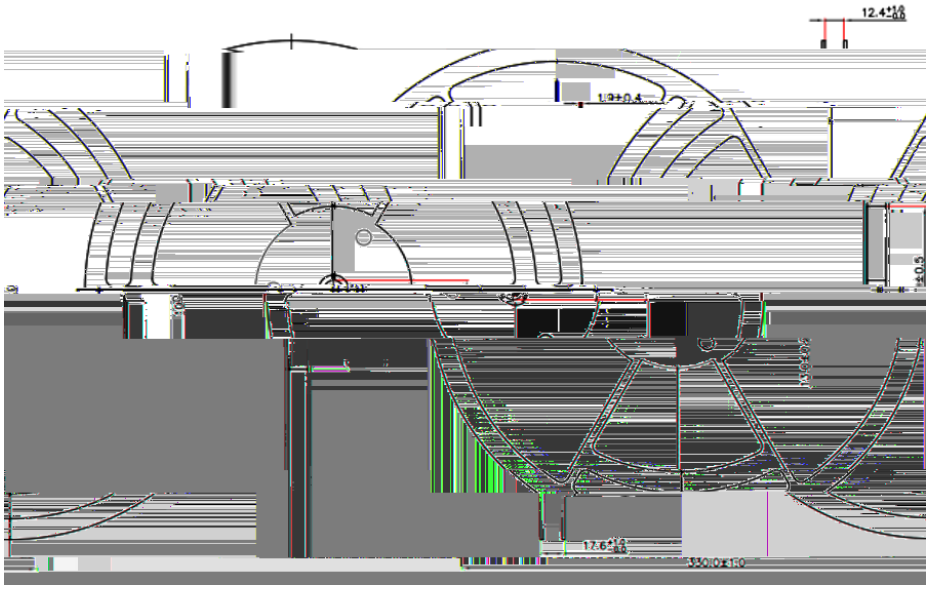

0.23±0.02
2.30±0.1
1.10±0.10

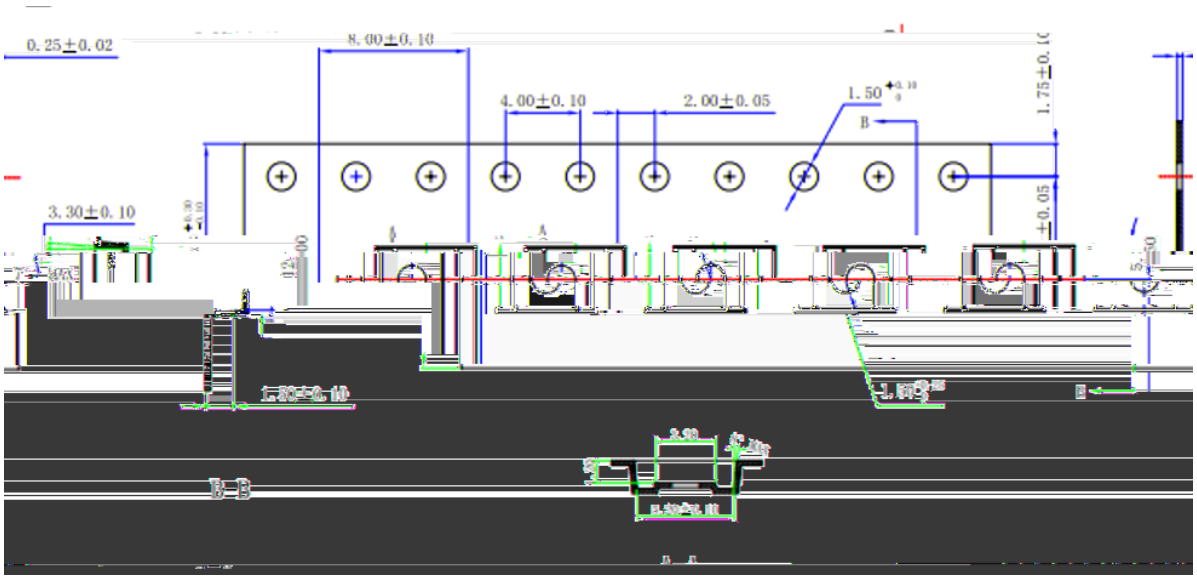

PIN1
User Direction of Feed

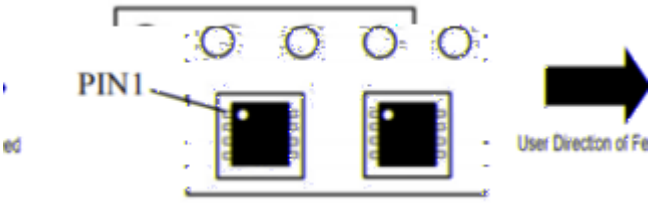

13.1±1.3
9.5±1

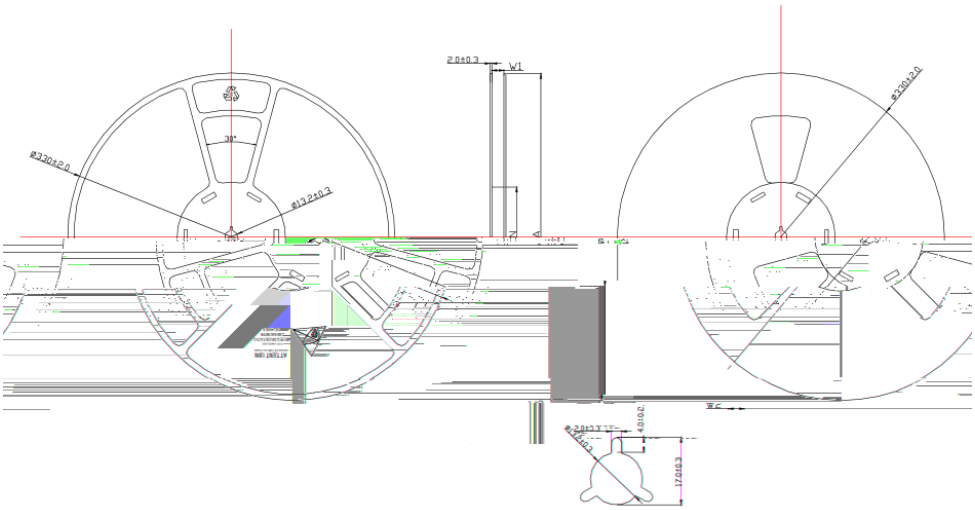


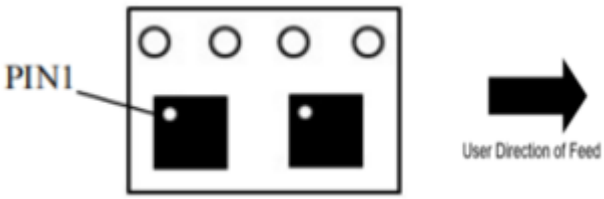

BIN1
2980

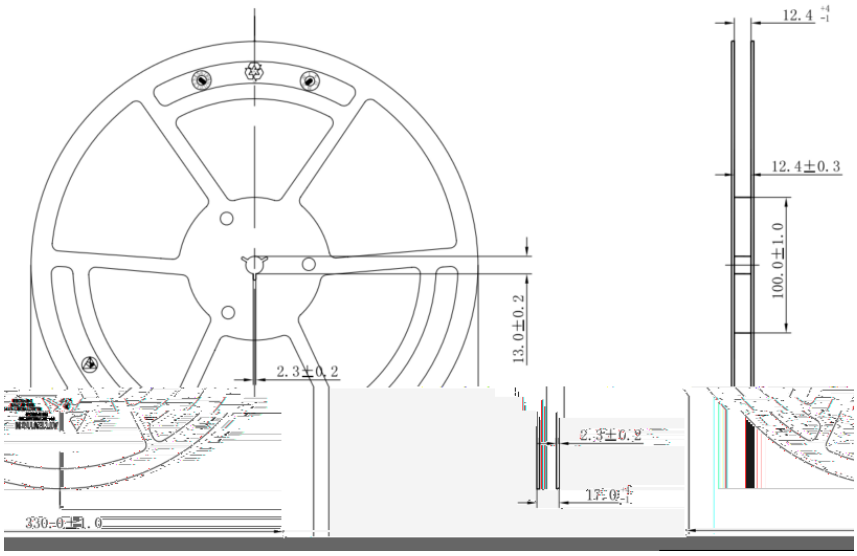


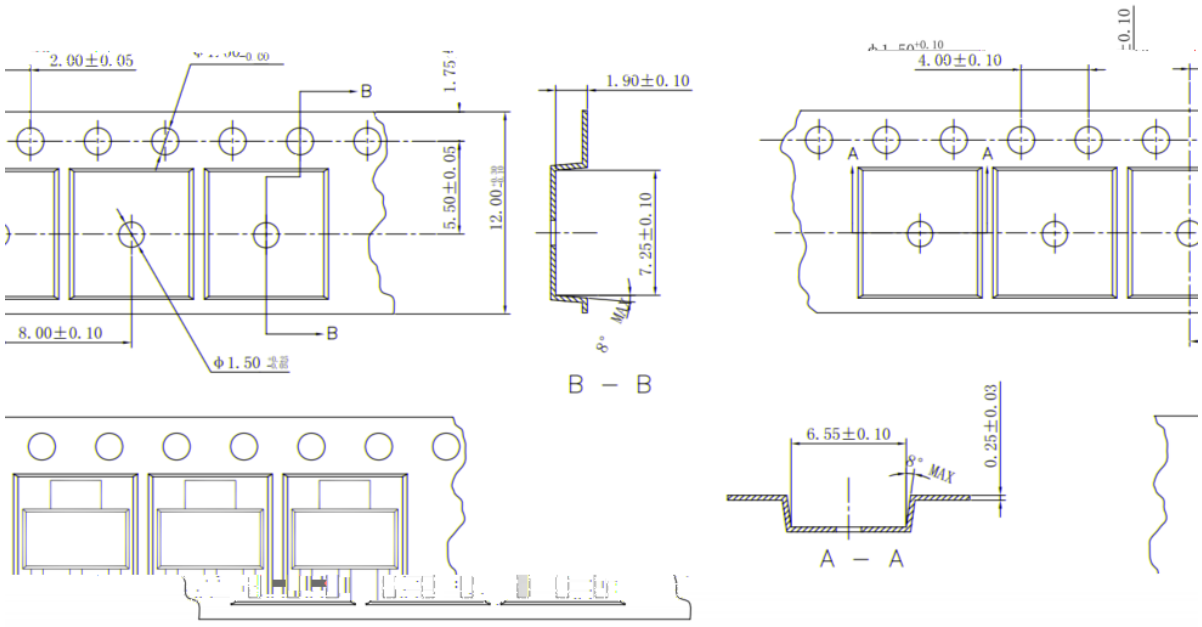

0.25±0.02
8.00±0.10
4.00±0.10
2.00±0.05
1.50
3.30±0.10
1.75±0.10
B-B


PIN1
User Direction of Fe




PIN1
User Direction of Feed





产品装载示意图

User Direction of Feed