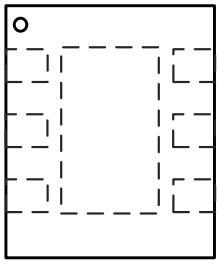
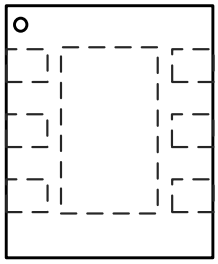
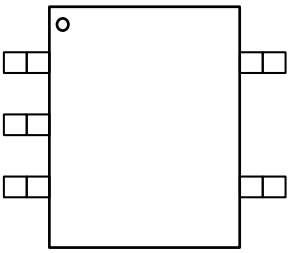
1.0: Release to production.

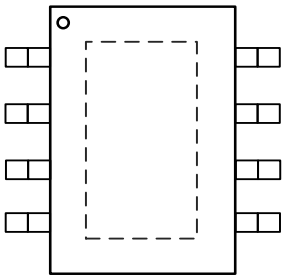
Revision 1.1: Update Figure 7.

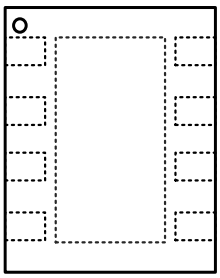
Revision 1.2: Update Part Number.











(1)

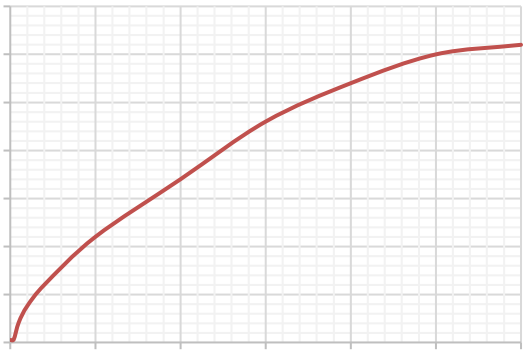
(2)

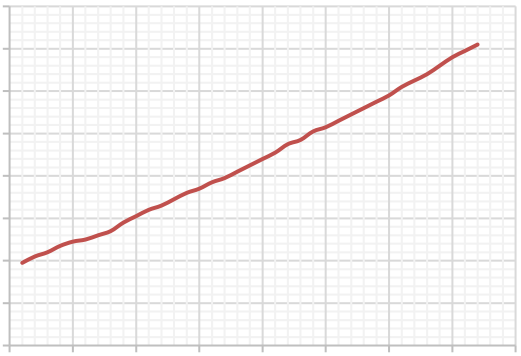
(1)
(2)

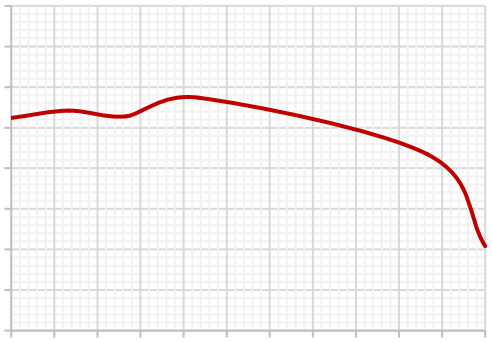
(1)

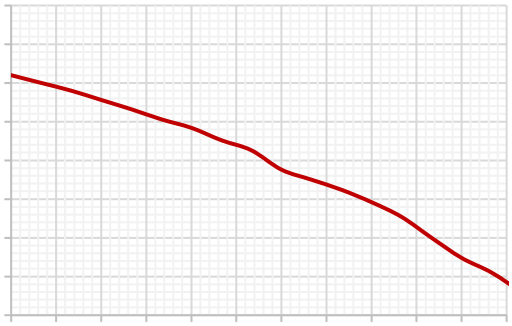
(2)

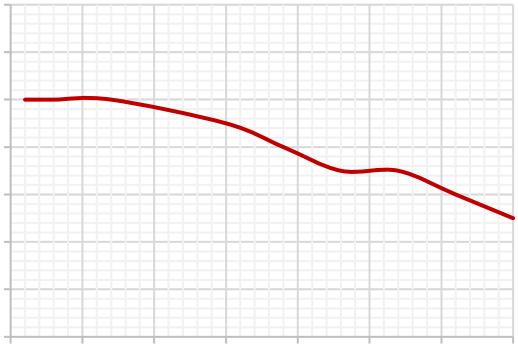
(3)

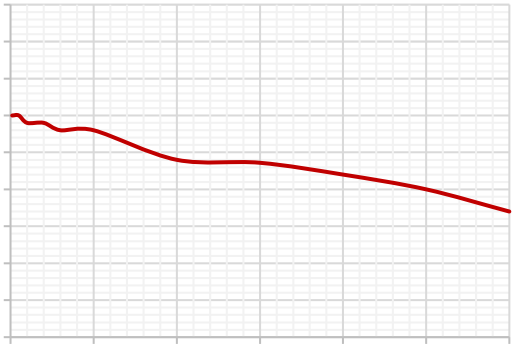


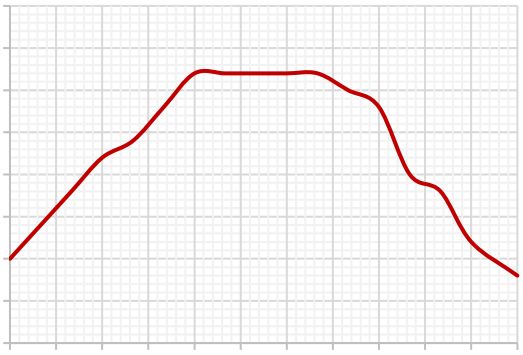


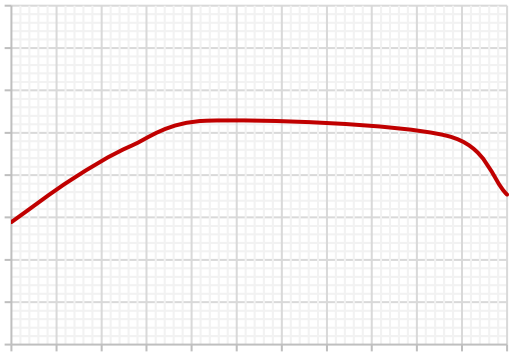


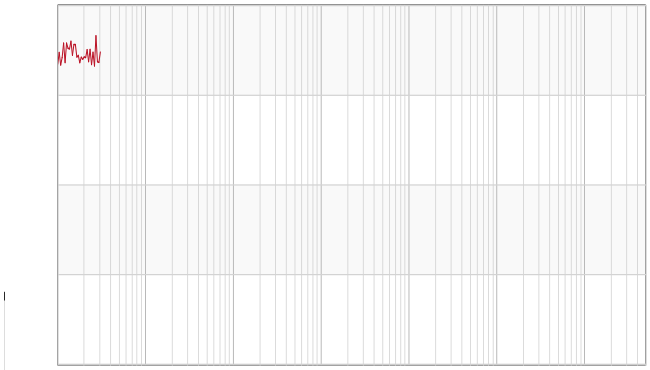


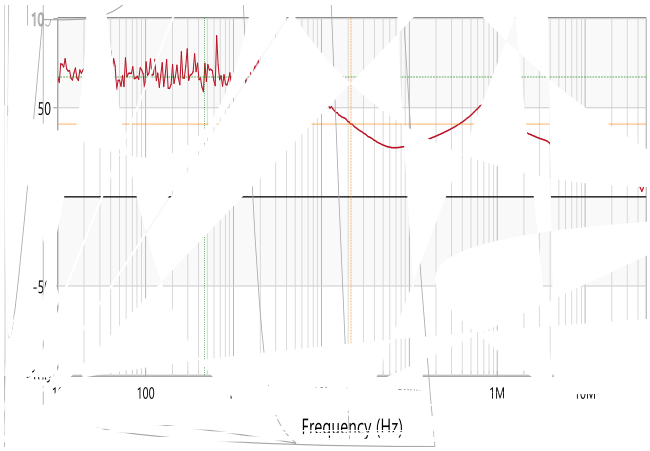


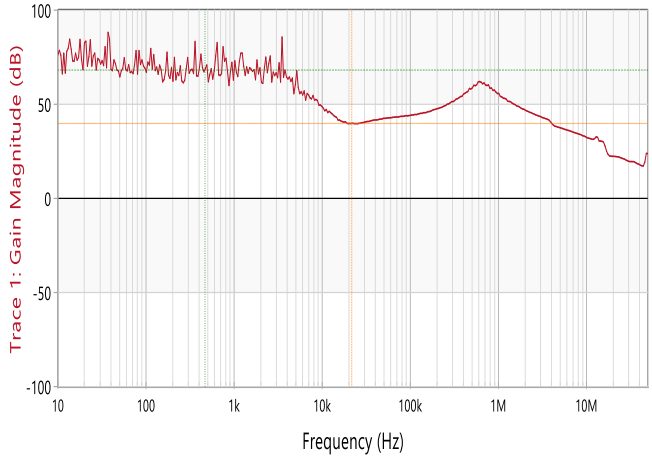


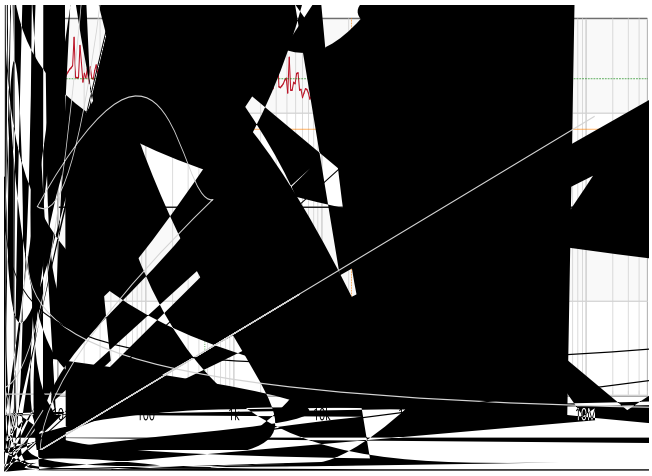


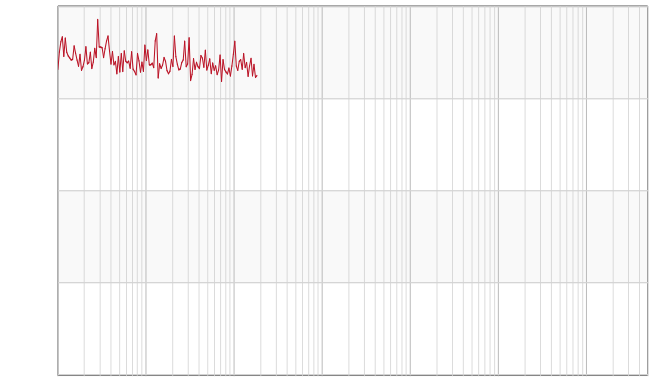


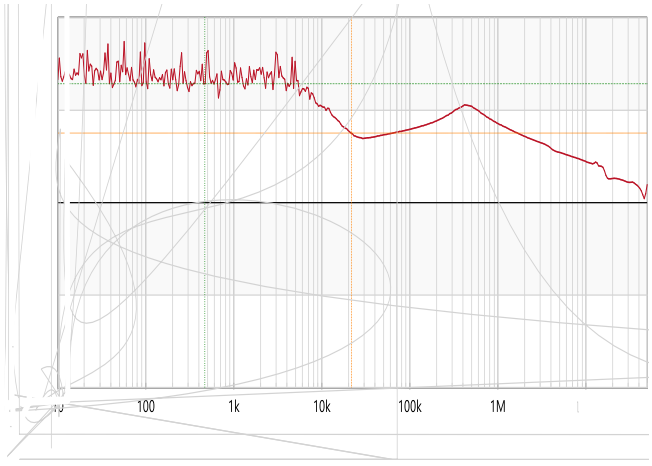


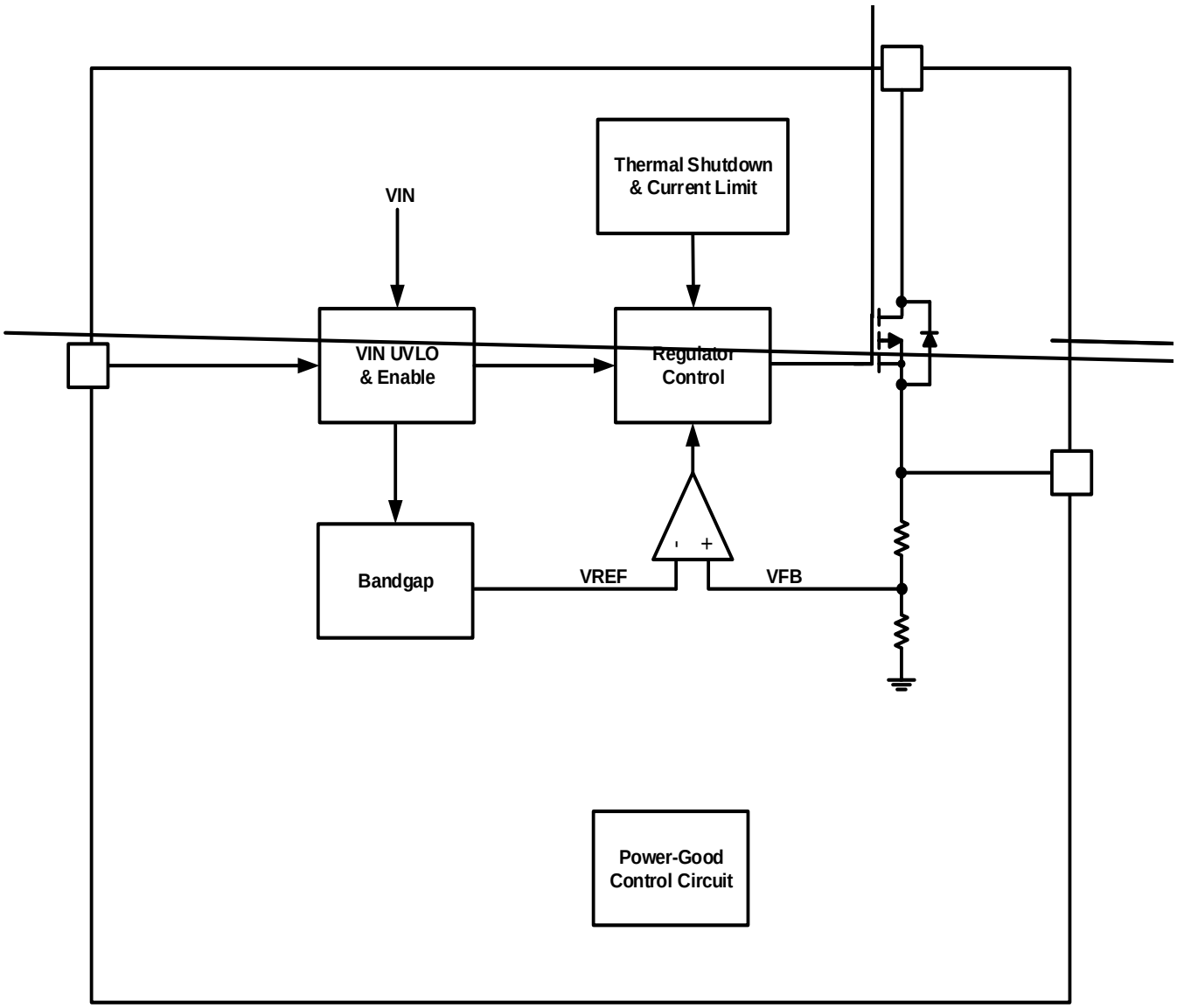

10
50
-5
100
1M
Frequency (Hz)


Trace 1: Gain Magnitude (dB)
100
50
0
-50
-100
10
100
1k
10k
100k
1M
10M
Frequency (Hz)



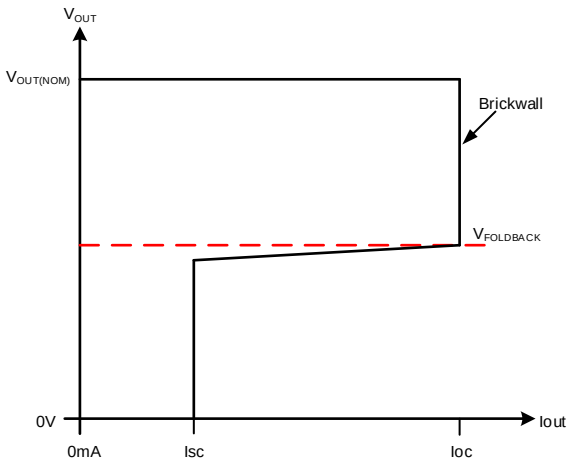



100
1k
10k
100k
1M


Thermal Shutdown
& Current Limit
VIN
VIN UVLO
& Enable
Regulator
Control
Bandgap
VREF
VFB
Power-Good
Control Circuit

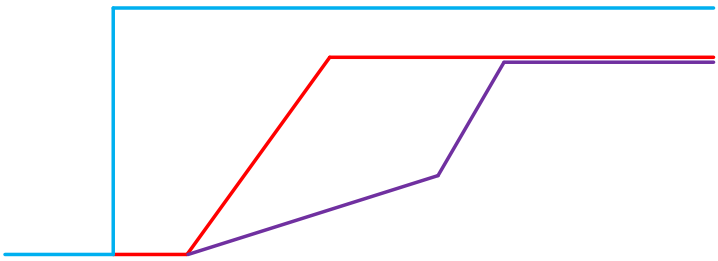
$$hT \quad C \quad \frac{1}{1}$$

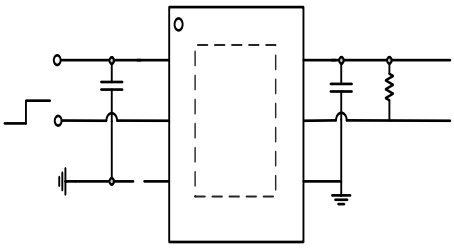
$$h \quad C \quad C \quad \frac{1}{1}$$

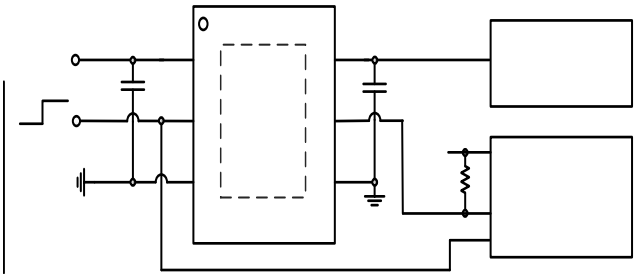


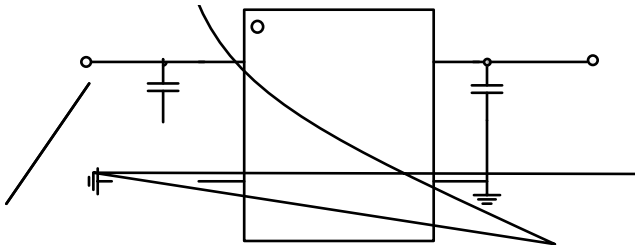
m ——— ——— I_{HH}

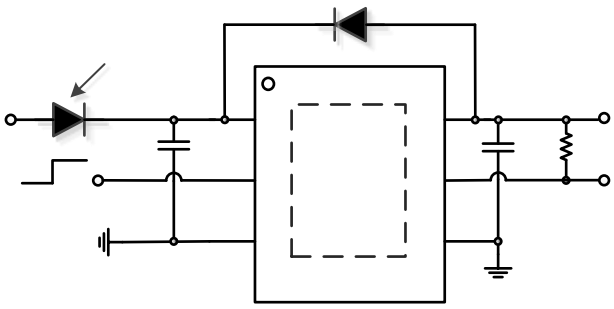
0 0

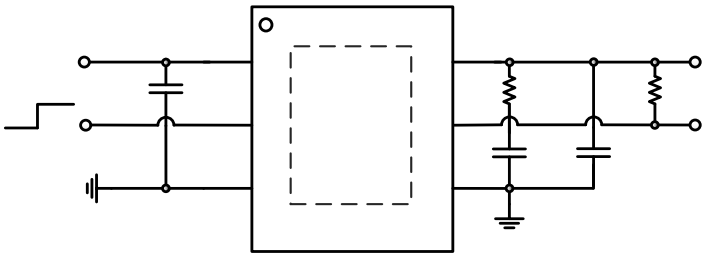


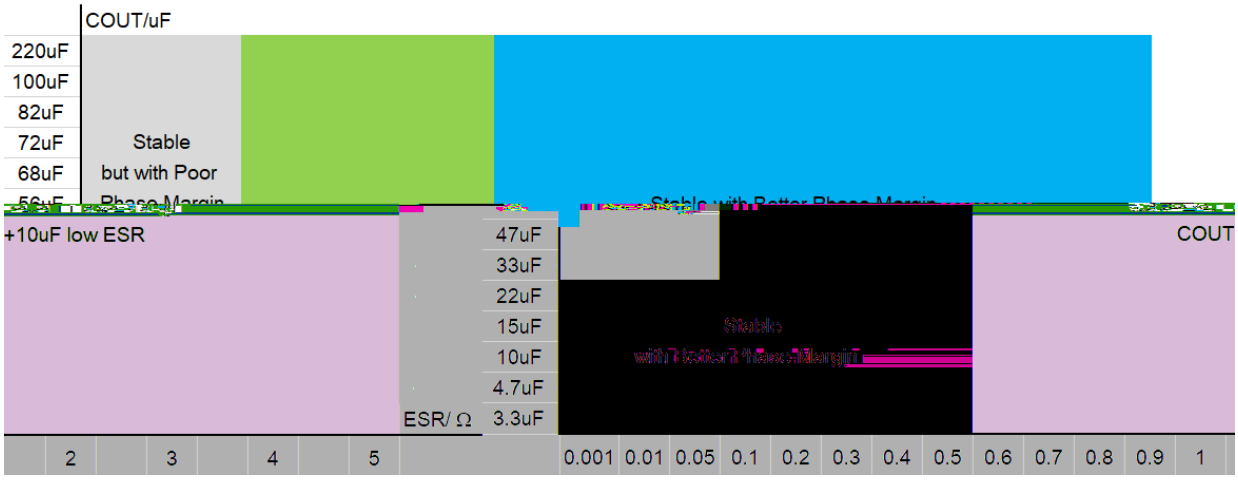


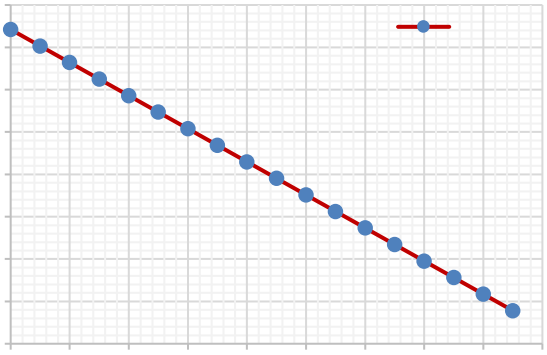


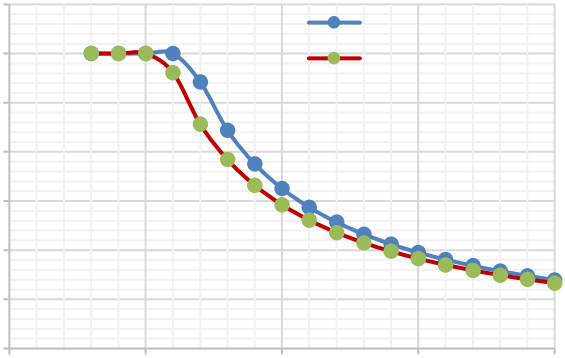


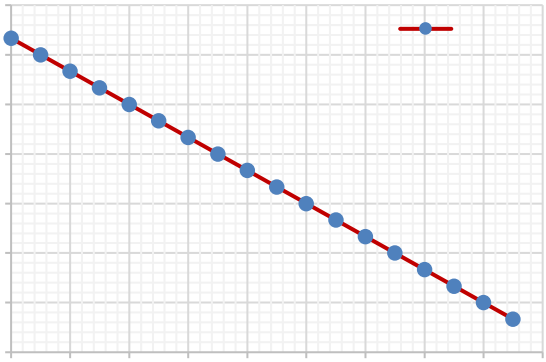


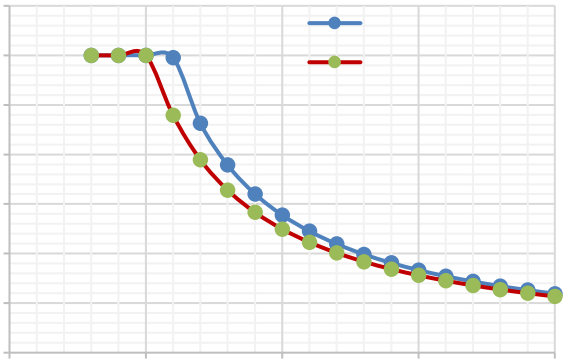


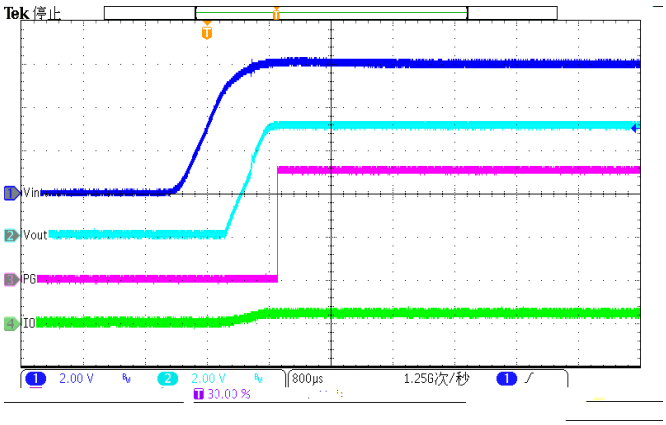

COUT/uF
220uF
100uF
82uF
72uF
68uF
Stable
but with Poor
+10uF low ESR
47uF
33uF
22uF
15uF
10uF
4.7uF
3.3uF
ESR/ Ω
COUT
2
3
4
5
0.001
0.01
0.05
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1

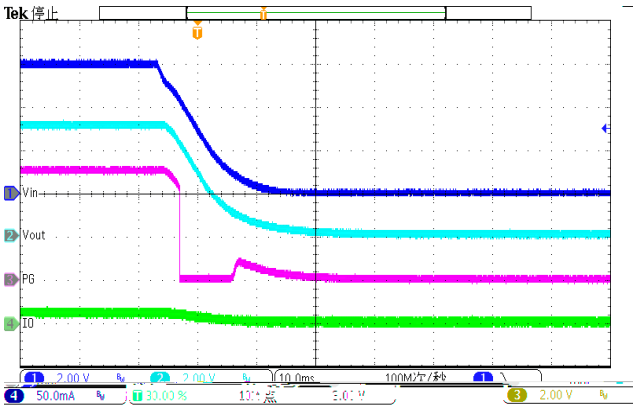


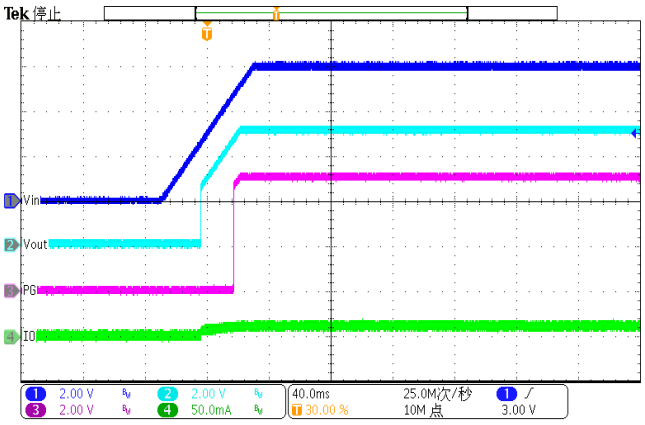


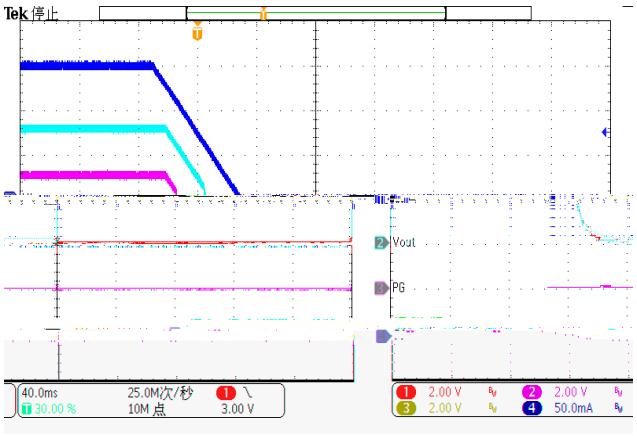


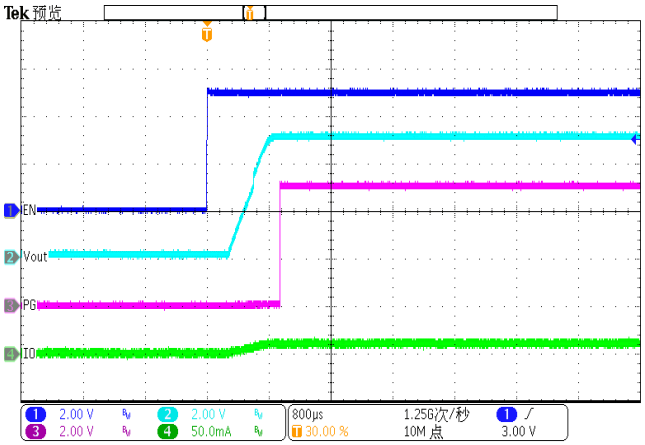


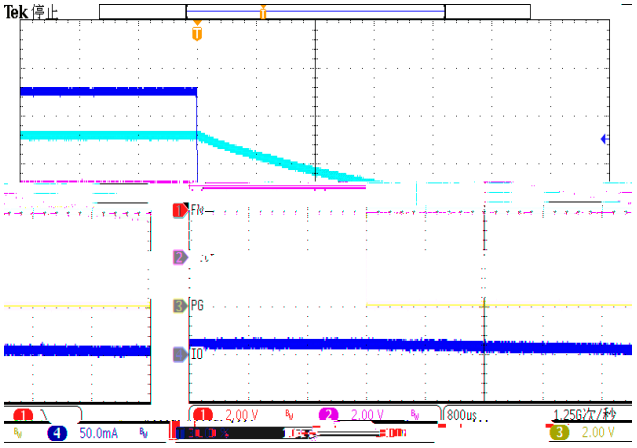

Tek 停止
Vin
Vout
PG
IO
2.00 V
800µs
1.25G次/秒
30.00 %

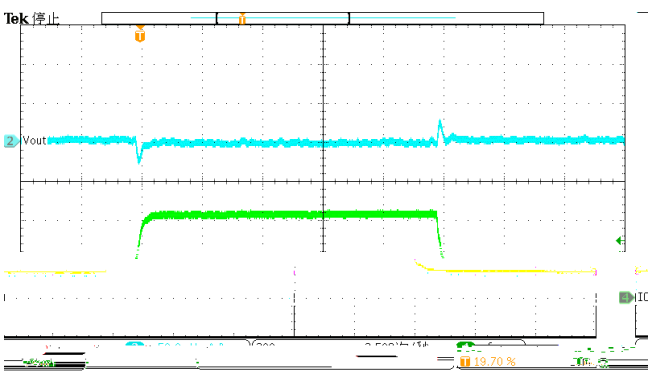

Tek 停止
Vin
Vout
PG
IO
50.0mA
30.00 %
100M次/秒
2.00 V

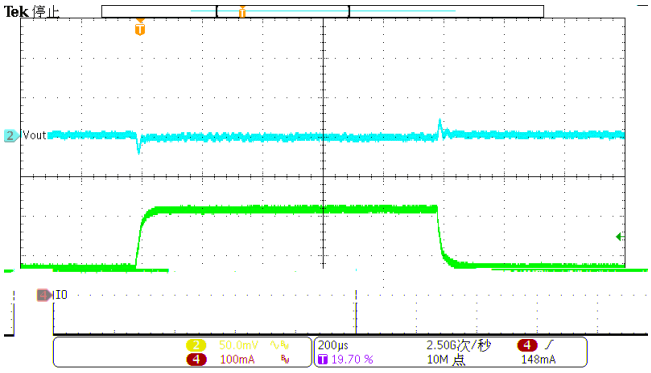

Tek 停止
Vin
Vout
PG
IO
2.00 V
50.0mA
40.0ms
30.00 %
25.0M次/秒
10M 点
3.00 V

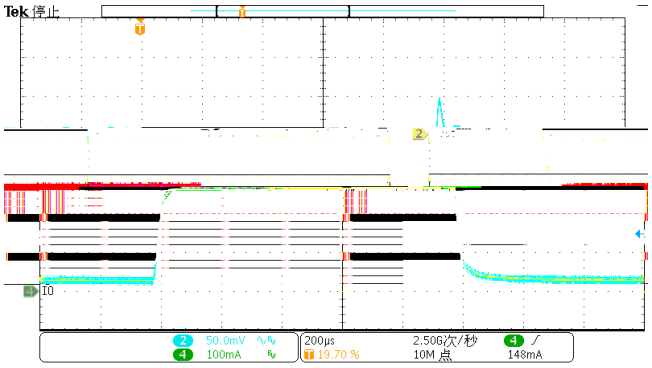

Tek 停止
Vout
PG
40.0ms
30.00 %
25.0M次/秒
10M 点
3.00 V
2.00 V
50.0mA

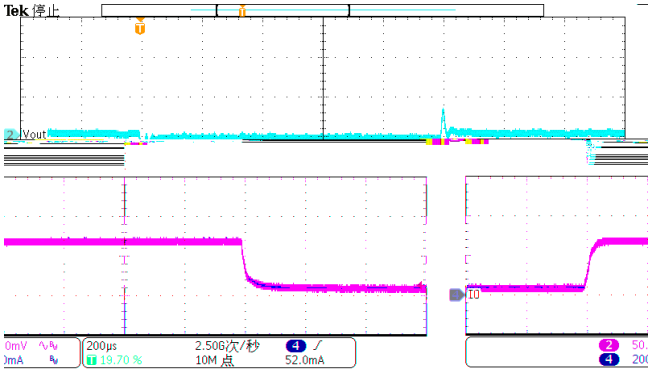

Tek 预览
EN
Vout
PG
IO
2.00 V
50.0mA
800µs
30.00 %
1.25G次/秒
10M 点
3.00 V

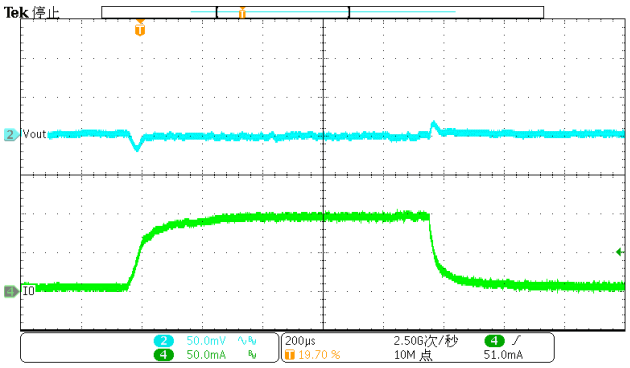

Tek 停止
EN
PG
IO
50.0mA
800µs
1.25G次/秒
2.00 V

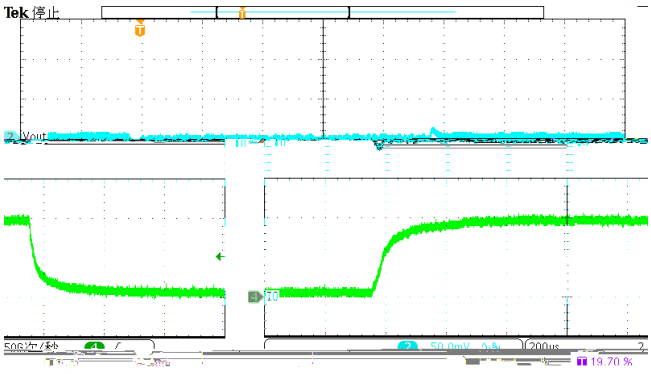

Tek 停止
Vout
IO
19.70 %

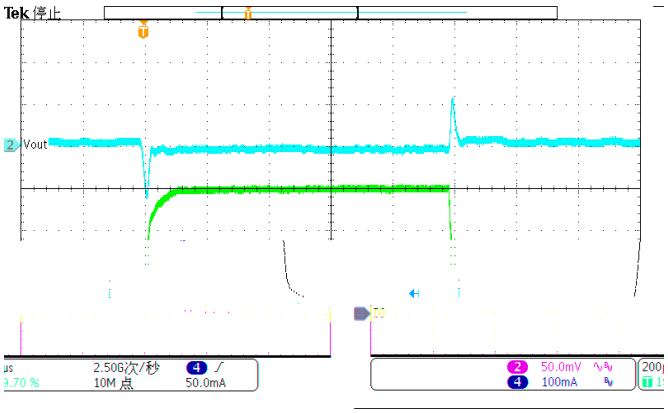

Tek 停止
Vout
IO
50.0mV
100mA
200µs
19.70 %
2.50G次/秒
10M 点
148mA

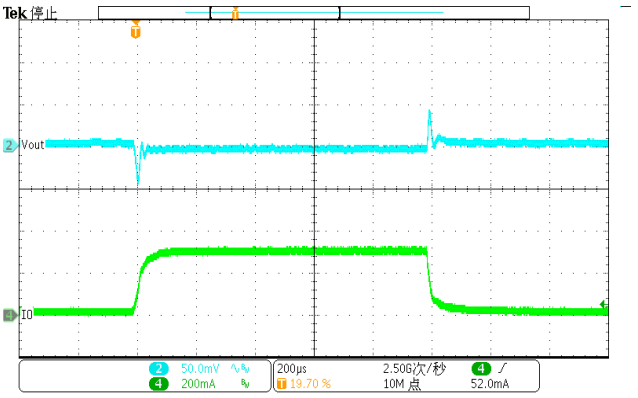

Tek 停止
IO
50.0mV
100mA
200µs
19.70 %
2.50G次/秒
10M 点
148mA

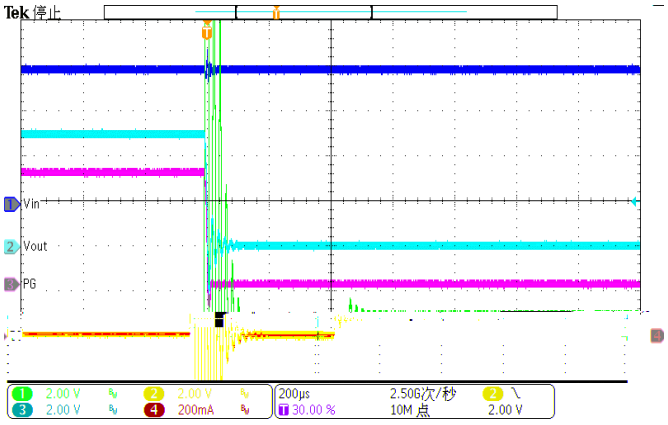

Tek 停止
Vout
IO
200µs
19.70 %
2.50G次/秒
10M 点
52.0mA

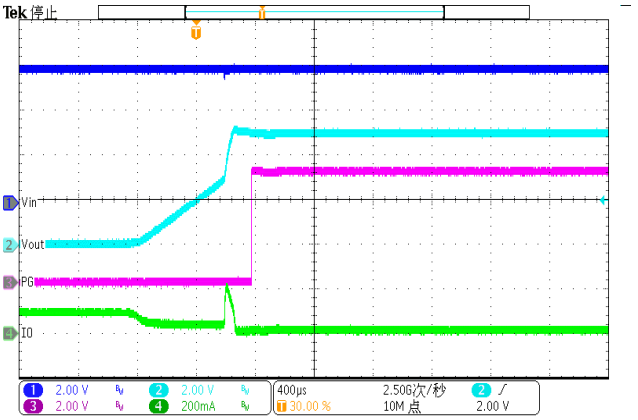

Tek 停止
Vout
IO
50.0mV
50.0mA
200µs
19.70 %
2.50G次/秒
10M 点
51.0mA

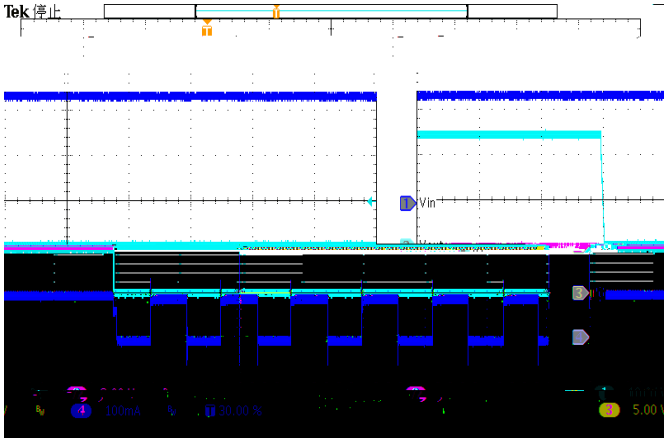

Tek 停止
Vout
IO
19.70 %

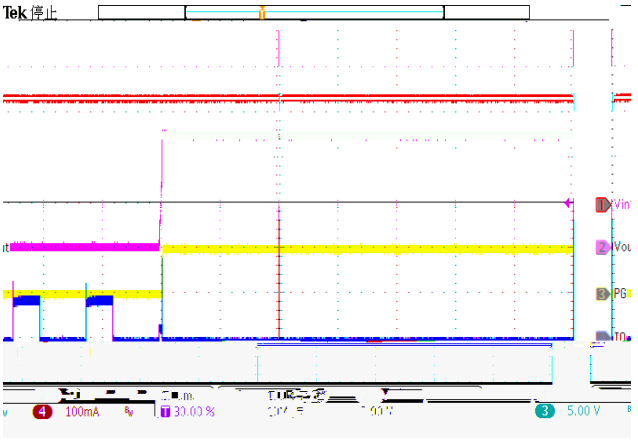

Tek 停止
Vout
IO
2.50G次/秒
10M 点
50.0mA
50.0mV
100mA


Tek 停止
Vout
IO
50.0mV
200mA
200µs
19.70 %
2.50G次/秒
10M 点
52.0mA


Tek 停止
Vin
Vout
PG
2.00 V
200mA
200µs
30.00 %
2.50G次/秒
10M 点
2.00 V


Tek 停止
Vin
Vout
PG
IO
2.00 V
200mA
400µs
30.00 %
2.50G次/秒
10M 点
2.00 V


Tek 停止
Vin
100mA
30.00 %
5.00 V


Tek 停止
Vin
PG
100mA
30.00 %
5.00 V

1.
2.

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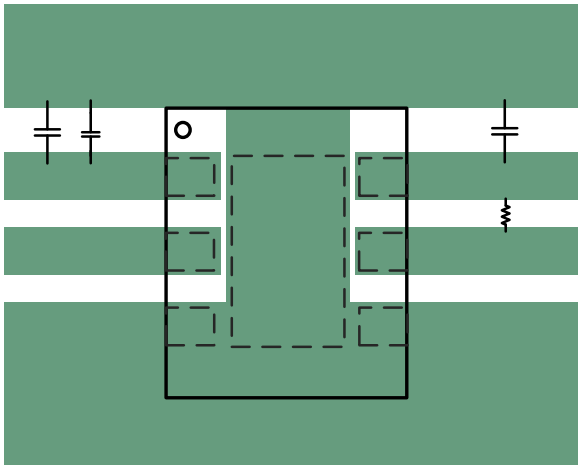
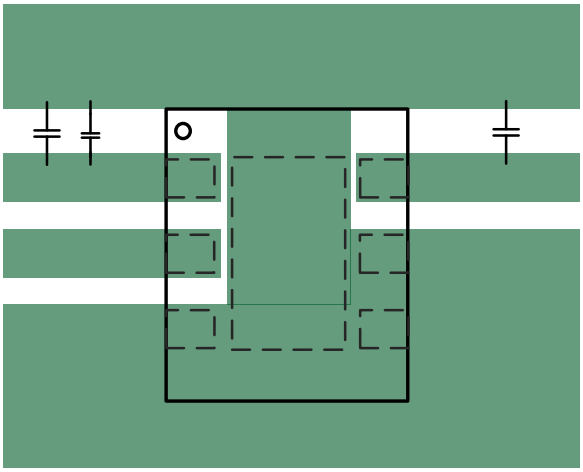
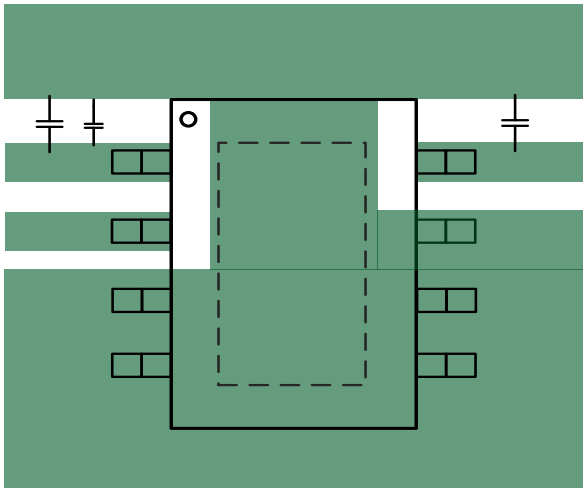
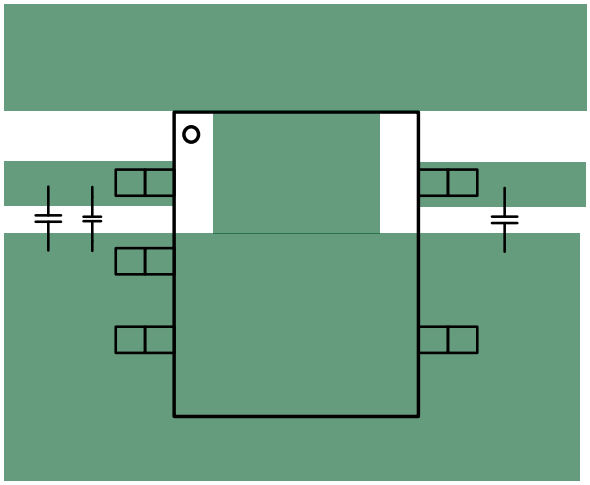
4.

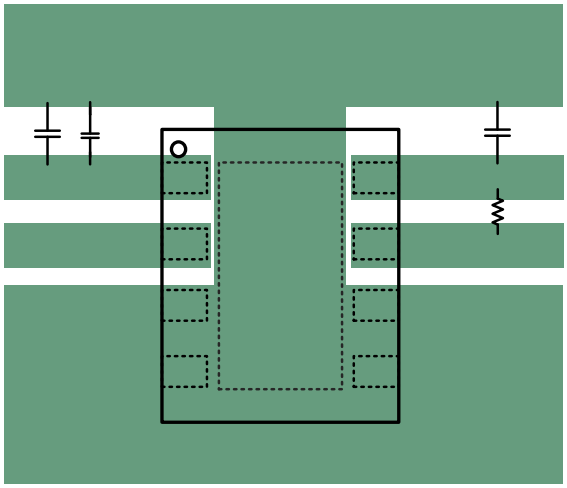
5.

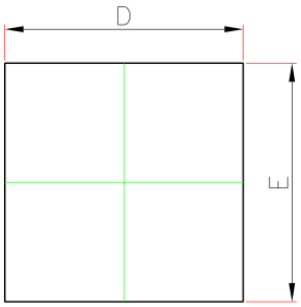




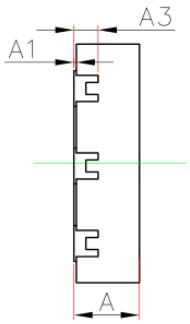




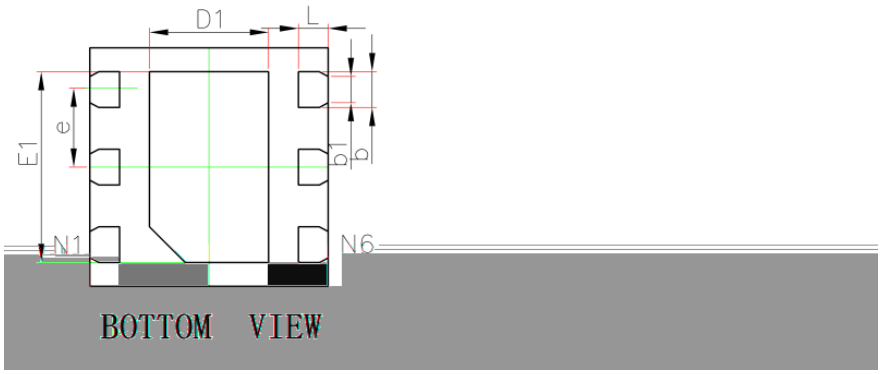




TOP VIEW



SIDE VIEW



1.
2.
3.
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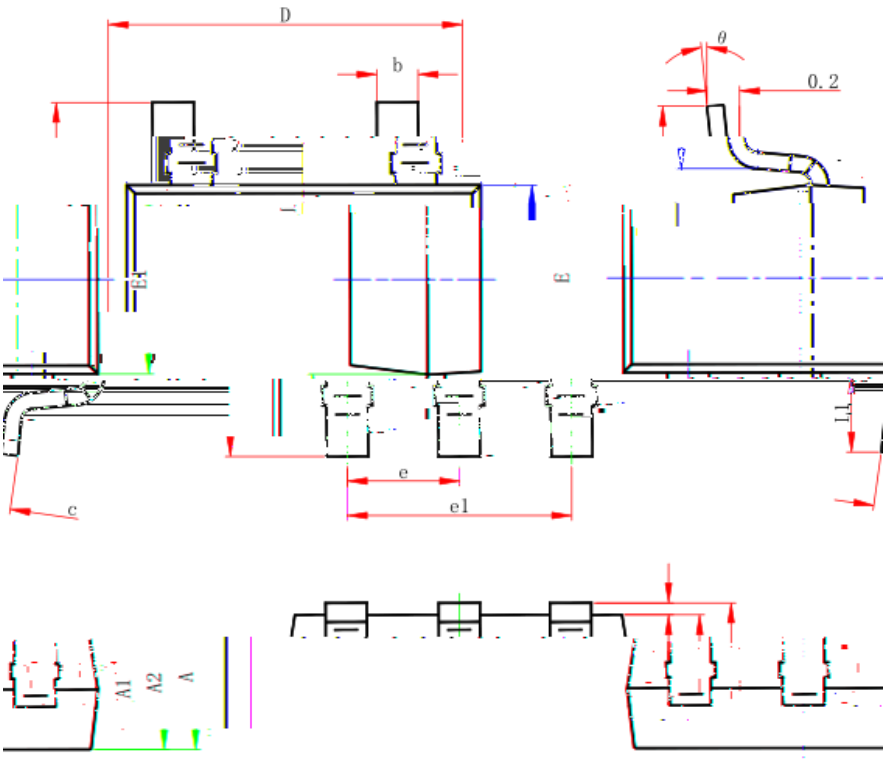
5.
6.



1.
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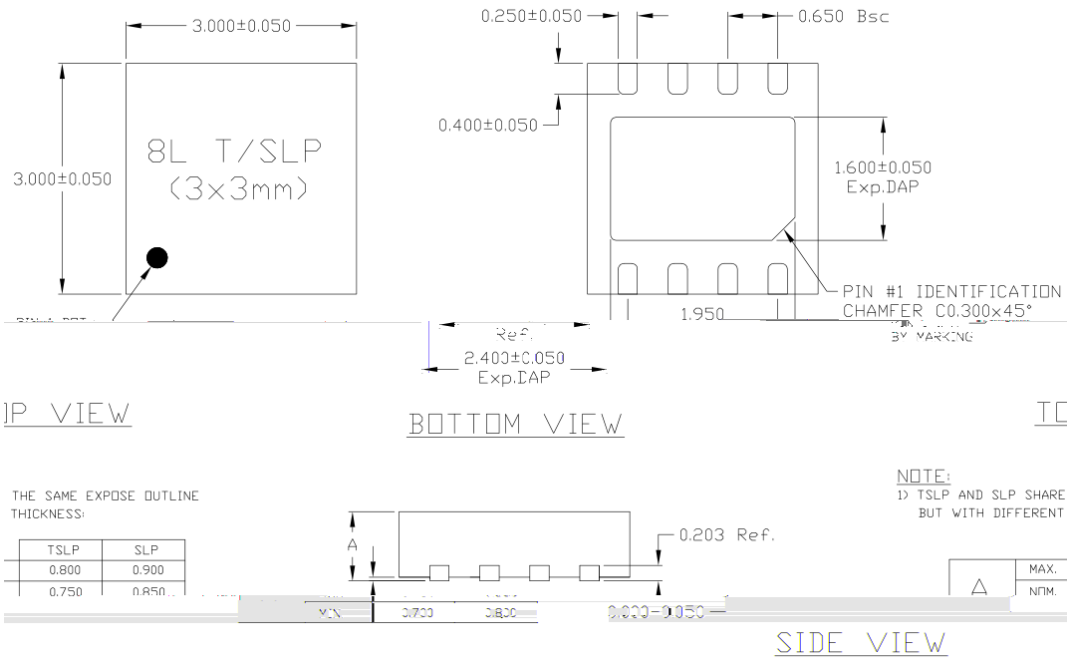
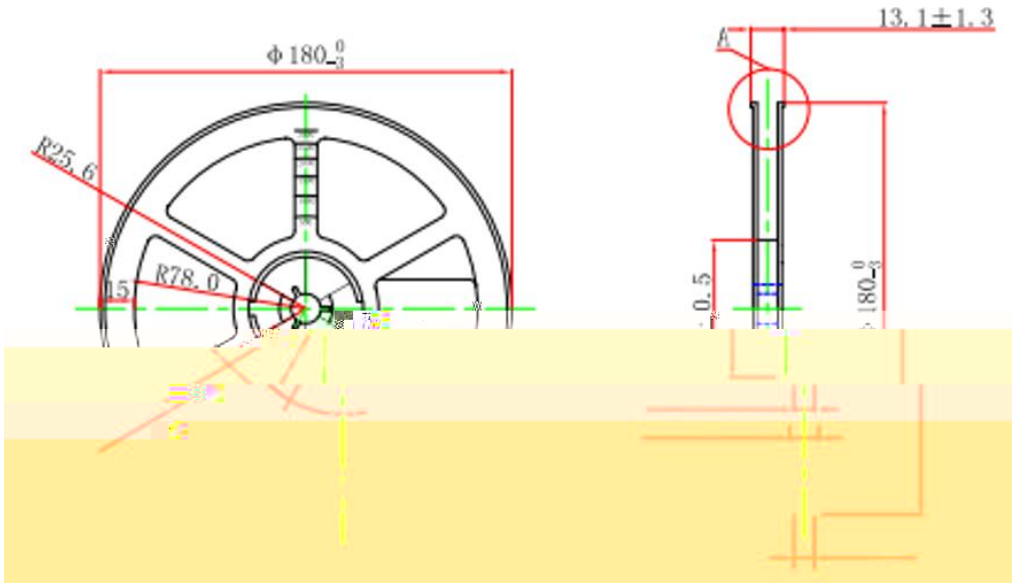
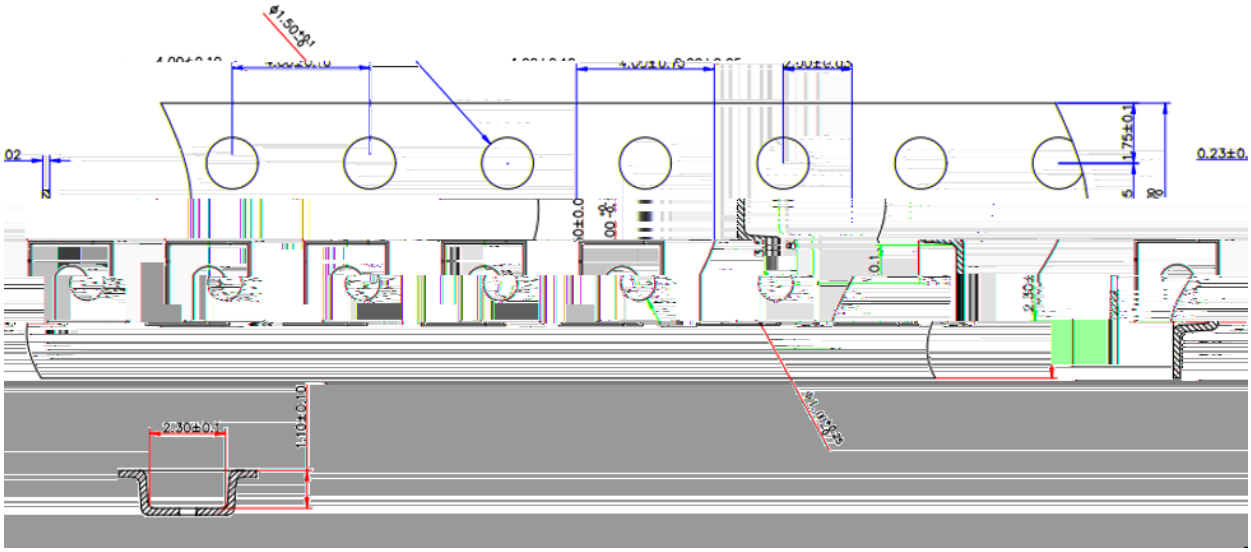
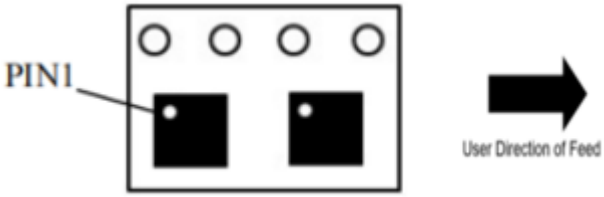
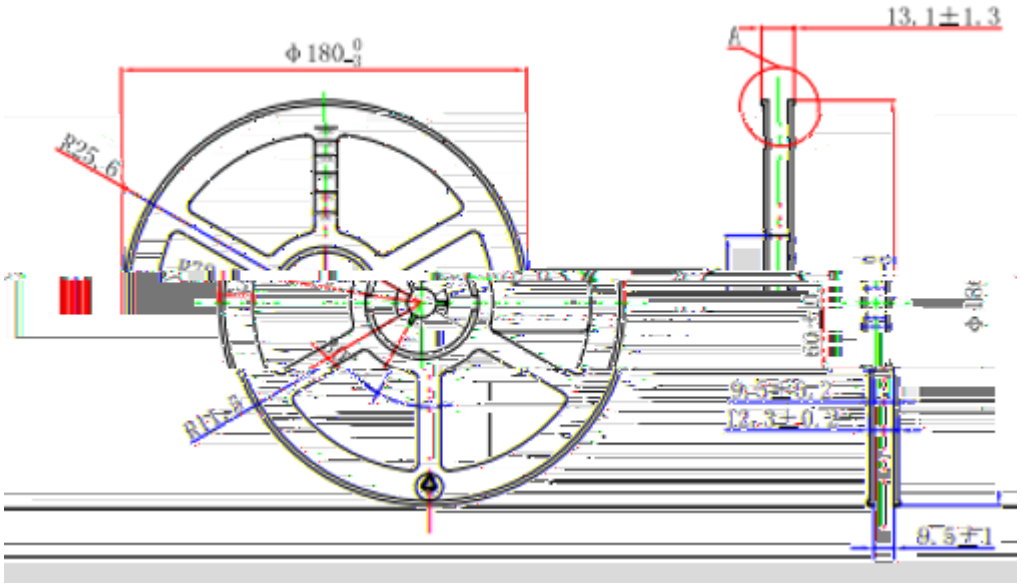

3.000±0.050
3.000±0.050
8L T/SLP
(3x3mm)
0.250±0.050
0.650 Bsc
0.400±0.050
1.600±0.050
Exp.DAP
PIN #1 IDENTIFICATION
CHAMFER C0.300x45°
1.950
2.400±0.050
Exp.DAP
BOTTOM VIEW
THE SAME EXPOSE OUTLINE
THICKNESS:
TSLP
SLP
0.800
0.900
0.750
0.850
A
0.203 Ref.
NOTE:
1) TSLP AND SLP SHARE
BUT WITH DIFFERENT
MAX.
NOM.
SIDE VIEW

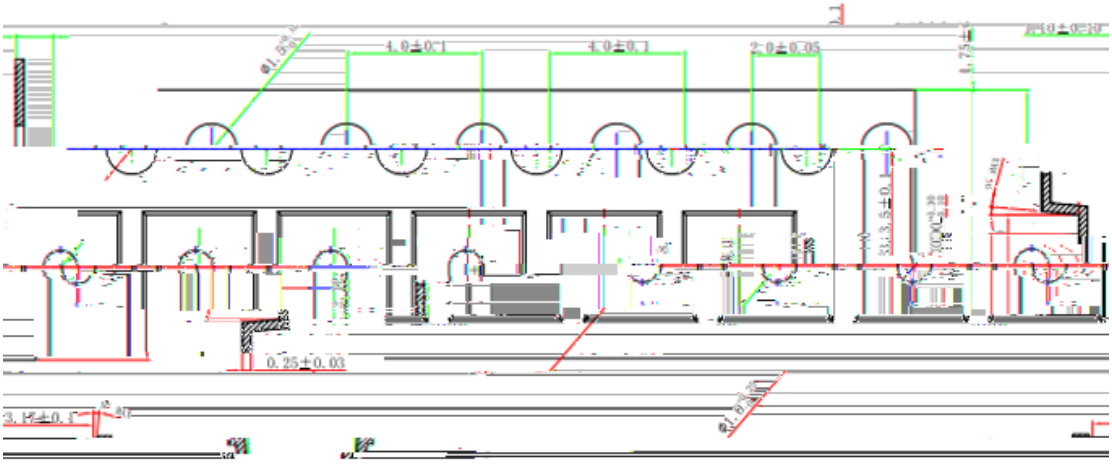
1.
2.
3.
4.
5.
6.

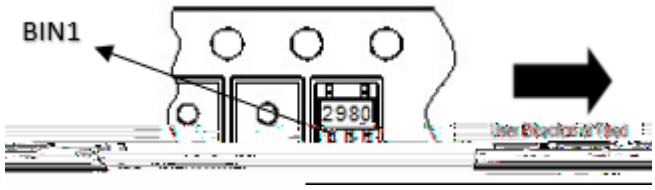

13.1±1.3
A
Φ180
R25.6
R78.0
15
0.5
180

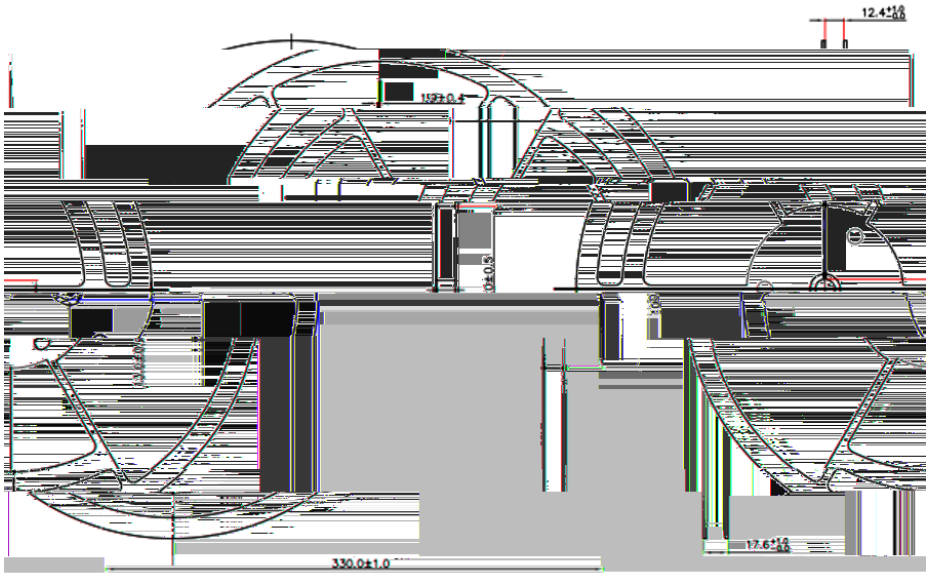

Φ1.5±0.1
0.23±0.
2.30±0.1
1.10±0.10

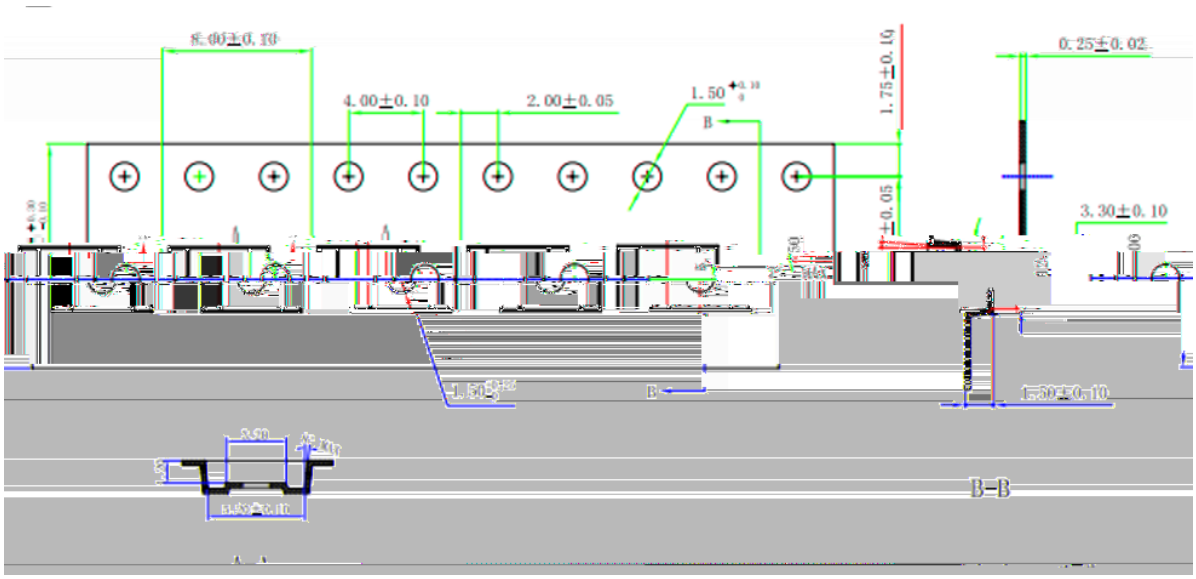

PIN1
User Direction of Feed

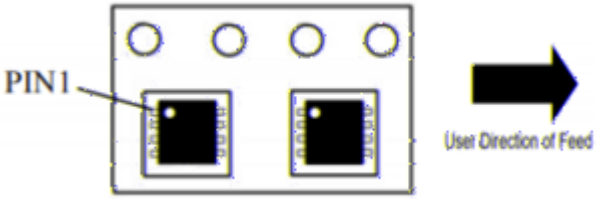

13.1±1.3
A
Φ180
R25.6
R17.8
8.5±1


4.0±0.1
4.0±0.1
2.0±0.05
0.25±0.03


BIN1
2980




8.00±0.10
4.00±0.10
2.00±0.05
1.50
1.75±0.10
0.25±0.02
3.30±0.10
B
A
B–B


PIN1
User Direction of Feed